#### CRITERIA 6.3.3

# BROCHURES OF PROFESSIONAL DEVELOPMENT/ADMINISTRATIVE TRAINING PROGRAMMES

#### CONTACT

#### Dr. Munish Rattan

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Department of Electronics &
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#### Er. .Narwant Singh Grewal

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**GNDEC**, Ludhiana **Phone**: 0161-5064765(O)

E-Mail:: ecefdp14@gmail.com

#### **GENERAL INFORMATION**

Guru Nanak Dev Engineering College, Ludhiana is the oldest and a premier Engineering Institute of India. It is an autonomous college under UGC Act 1956 [2(f) and 12(B)]. The Institute is setup on 88 acres of sprawling pristine land on Ludhiana-Malerkotla state highway. Ludhiana city is a well known Industrial hub in Northern India and is well connected by direct rail service with all parts of India. The beautiful campus is about 7 kms away from railway station. The weather in the month of July in Ludhiana is hot with temperature around 42°C.

#### REGISTRATION

There is no Registration fee for the participants from AICTE recognized Institutes. Participants from industry/ non AICTE institutes shall have to pay registration fee of Rs. 1000/-

# **TEQIP-II Sponsored Faculty Development Programme**

on

Nature Inspired Computational Techniques for Engineering and Sciences

(July 21, 2014 to July 25, 2014)



#### **Chief Coordinator**

Dr. Sandeep Singh Gill

#### **Coordinators**

Dr. Munish Rattan Er. Narwant singh Grewal

#### Organized by

Department of Electronics & Communication Engineering

GURU NANAK DEV ENGINEERING COLLEGE

Ludhiana, Punjab - 141006

Phone No. 0161-5064765 E-mail: ecefdp14@gmail.com

#### **INTRODUCTION**

The Nature Inspired Computational techniques are the Numerical and Computer based techniques for solving a variety of practical problems that arise in different fields. Natural Computing coveres three classes of methods which includes inspiration from nature for the development of novel problem solving techniques, use of computers to synthesise natural phenomenon and employs natural materials to compute. The major techniques under this field includes: ANN, Swarm Intelligence, FA, Artificial Immune system, , DNA Computing etc.

#### **COURSE CONTENTS**

The aim of the course is to provide the participants understanding of Nature Inspired Computing Techniques and its Applications. At the end of the course, the participants are expected to have a fair understanding of:

- **▶** Nature Inspired Computing Techniques
- Particle Swarm Optimization and its Applications
- Biographical Based Optimization and its Applications
- Firefly Algorithm and its Applications
- **Bat Algorithm and its Applications**
- Hybrid Algorithms and their Applications
- Other Nature Inspired Algorithms

#### **FACULTY**

Experts from IITs, NITs and other renowned academic Institutions besides faculty from our institute will deliver expert talks.

#### **ELIGIBILITY CRITERIA**

The program is interdisciplinary in nature and has been designed for all Engineering, Sciences & Management Faculty, professionals, practicing Engineers, and PG students.

#### **BOARDING & LODGING**

Accommodation shall be provided in college hostels free of charge. However, guest house/ hotel/lodge may be booked on payment basis if intimated well in advance.

#### **IMPORTANT DATES**

Last date for Receiving Application: July 15, 2014 Notification of Acceptance: July 17, 2014

#### Nature Inspired Computational Techniques for Engineering and Sciences (July 21,2014 to July 25,2014)

#### **APPLICATION FORM**

Name:	
Father's Name:	
Date of Birth:	
Academic Qualificati	on:
Designation:	
Address of Sponsori	ng Authority:
Experience (years) :	
Mobile No	
E-Mail:	
Accommodation requ	uired : Yes/No
Date:	Signature of Applicant

#### **SPONSORSHIP CERTIFICATE**

The applicant will be permitted to participate in the above programme, if selected. It is also certified that this institute is recognized by AICTE.

Signature of sponsoring authority

TITLE: Workshop on VLSI Design (foundation course)

Place: GNDEC Ludhiana, ECE Dept

Date: 14<sup>th</sup>-15<sup>th</sup> October, 2015.

**Objective**: To incite passion amongst participants to take career in most exciting field of engineering – Space Technology and Microelectronics. In this brief course, some glimpses of space technology and foundation course on Integrated Circuit Design is planned.

Instructors: Sh HS Jatana, Sci/Engr 'G'; Group Director – Design & Process Group, SCL / ISRO, DoS

Sh Munish Malik, Sci/Engr 'E' Design & Process Group / VDD

#### Schedule:

DAY 1 (	14 <sup>th</sup> Octobe	r, 2015)			
Sr.	TOPIC		Instr	Contents	Start Time
No			uctor		
LECT1	Space and	Avionics	HSJ	Brief about ISRO, Space technology, satellites and launch Vehicles, use of microelectronics in space	11:00AM
LECT2	VLSI Indust	ry Overview	HSJ	Semiconductor market, process attributes, trends and issues, challenges in VLSI	12:00 Noon
BRK1	Lunch	Light food only aroun	d 45 mii	ns break 01:00PM	
LECT3	Foundation	ns of analog design - I	HSJ	Non linear circuits, small signal analysis	01:45PM
LECT4	Foundation	ns of analog design - II	HSJ	2-port NL network, y-parameters,	02:45PM
BRK2	Tea	Brief break of 15 mins	5	03:45PM	
LECT5	Foundation	ns of analog design - III	HSJ	Amplifier basics, maximum intrinsic gain achievable, characteristics	4:00PM
BRK	Closing at 5	5:00PM or based on partic	ipants c	onvenience.	
	Participants staying in College campus are free to interact, raise queries with us in the evenings/during dinner.		vith us in the		
DAY 2 (	15 <sup>th</sup> Octobe	r, 2015)			

LECT6	CMOS Analog Design – I	HSJ Single Tx configuration, CS, small signal and large signal analysis	AM
LECT7	CMOS Analog Design –II	HSJ Finding swing limits, gain 10:00 stability	DAM
BRK1	Tea Break for 1	5 Mins 11:00AM	
LECT8	CMOS Analog design –III	HSJ Frequency response analysis, effect of poles and zeroes	5AM
LECT9	CMOS Analog design – IV	HSJ Controlled sources, Design of Voltage Buffer, small signal and large signal analysis	5PM
BRK2	Light Lunch	1:00 - 1:45PM	
LECT10	Analog Simulations – I	MM Analog Simulations, 1:450 simulations of CS stage with Resistive load	PM
LECT 11	Analog Simulations – II	MM Analysis with active load, gain maximization, frequency response depiction through simulations	PM
BRK3	Tea 15 mins break	3:45PM	
LECT12	Analog Simulations – III	MM Voltage buffer, circuit and 4:000 its analysis	PM
		NANA Tast seem seems as a seed a AFI	
LECT13	Analog Simulations – IV	MM Test case as an example 4:45	PIVI

#### Note:

- 1. Each lecture to be around one hour duration.
- 2. Multimedia and white board is required.
- 3. Soft copy of material would be provided by instructors.
- 4. Emphasis on learning design concepts and fundamentals in this course.
- 5. Participants and organizers are requested to provide feedback on this course.

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#### Three Day Workshop on Quality Initiatives in Technical Educational Institutions (24-26 December, 2015)



Coordinator Dr. Akshay Girdhar

Co-Cordinator **Dr. Rupinder Singh** 

Organized By
Guru Nanak Dev Engineering College,
Ludhiana, Punjab-141006

[An Autonomous College under 2(f) and 12(b) of UGC Act 1956]

E-mail: workshops@gndec.ac.in, workshop.gne@gmail.com

#### **About Institute**

Guru Nanak Dev Engineering College (GNDEC), Ludhiana is one of the oldest and a premier Engineering Institute of India. The foundation stone of the college was laid by Honorable Dr. Rajendra Prasad Ji, President of India on 8<sup>th</sup>April, 1956. The institution runs seven B.Tech., thirteen M.Tech., MBA and MCA courses, besides being a QIP centre for Ph.D. The undergraduate programmes of the Institute are accredited since 2004. Presently, all the undergraduate programmes of the institute are accredited by National Board of Accreditation (NBA) under Tier-I (Washington Accord). The institute is also accredited by Tata Consultancy Services and National Assessment and Accreditation Council (NAAC). The institute is set up on 88 acres of sprawling pristine land on Ludhiana-Malerkotla highway in Punjab.

#### **About Workshop**

Quality is everyone's prerogative and everyone's responsibility. The technical education system in India is been subjected to radical and revolutionary changes and is the significance of quality initiatives and is not oblivious to its benefits reaped the manufacturing sector. The growth and competition among self-financing institutes and ever increasing awareness of stakeholders has pressurized the technical educational system to shift its focus from quantitative expansion to qualitative expansion. Total quality management in technical education previews the quality of inputs in the form of students, faculty, teaching learning process and infrastructure; and the quality of outputs in the form of the value added students with a host of indicators assessing quality. The drive for NBA & NAAC accreditation symbolizes growing interest in applying quality initiatives in technical institutes. The focus is now shifting from output based system to outcome based. The implementation and assessment of quality in education goes a long way in continuous improvement of the organization and satisfaction of the stakeholders.

With the above issues in focus, Guru Nanak Dev Engineering College, Ludhiana has planned a three day workshop on "Quality Initiatives in Technical Educational Institutions".

#### **Objective**

The proposed workshop aims to understand and effectively implement:

- Outcome based education model
- Outcome based accreditation process of NBA
- Accreditation criteria of NAAC

#### Coverage

- Introduction to outcome based education
- Process of defining vision, mission
- Formulation of Programme Education Objectives (PEO), Programme Outcomes (PO) and Course Outcomes (CO)
- Design of direct and indirect assessment tools (like Rubrics, Microlevel Analysis, Course Embedded Surveys etc.) for computation of attainment
- Role of Revised Bloom's Taxonomy in design of course embedded assessment tools.
- Demonstration of Software tools for assessment.
- Preparation of Self-Assessment Report/ Self- Study Report
- Exercises etc.

#### Who can apply?

The programme is designed for the Principals, academicians, faculty members, and professionals, who are keen to explore in depth the processes related to NBA, NAAC accreditation and other quality initiatives in technical education. The institutions that are planning to go for NBA / NAAC Accreditation will be benefited by participating in this workshop.

#### **Programme Venue, Dates and Timings**

Venue: Consultancy Cell of the College

*Dates*: 24 – 26 December, 2015

Timings: 9 AM to 5 PM with breaks in between for tea and lunch.

#### Resource Person

Resource persons to conduct the workshop are from in-house expert faculty & outside experts from reputed and established educational entities in India with a strong record of accomplishment in quality issues in professional colleges.

#### **Boarding and Lodging**

Accommodation is available in the college Guest House on reasonable charges on prior request. Other arrangements for stay can also be made as per the requirement. The participants will have to bear these expenses (not included in registration fee).

#### **Registration Fees**

Registration fee for the workshop is Rs.5000 per participant. However, if there are two participants from the same Institute/ University then every third participant will get 50% discount in the registration fee. The fees include workshop kit, refreshment and lunch during the days of event.

Payments can also be transferred online by due date (10<sup>th</sup> December, 2015).

Bank Name: Punjab and Sind Bank Account Name: Director, GNDEC Account No: 00211000050614

Account Type: Saving

Address: Guru Nanak Dev Engg., Gill, Ludhiana-141006

IFSC Code: PSIB0000021

Send us the details of the transaction (or the scan copy of the transaction slip) to verify the payment. Please specifically ask your bank to write your name (Participant name) in the transaction notes.

#### How to register for the workshop?

Please visit http:// www.gndec.ac.in for online registration, and pay the requisite fee by electronic transfer or send a demand draft in favour of "Director, Guru Nanak Dev Engineering College", payable at Ludhiana. The proof of payment must be sent by email at workshops@gndec.ac.in, workshop.gne@gmail.com. The registration for limited seats will be done on first-come-first basis upto 10<sup>th</sup> December, 2015.

#### How to reach?

The college is located at a distance of 6 Km from Bus Stand, 8 Km from Railway Station, and 115 Km from Chandigarh Airport. There is availability of all sorts of public and private transports from stated places. The nearest landmark to the college is the Gill Canal.

#### **Address for Correspondence**

Dr. Akshay Girdhar

Associate Professor,

Dept. of Information Technology

Guru Nanak Dev Engineering College, Gill Road, Ludhiana – 141006 (PUNJAB)

Ph.: (O) 0161-5064548, (M) 098559-61620

E-mail: workshops@gndec.ac.in, workshop.gne@gmail.com



# TEQIP-II Sponsored Two Day Workshop on



# Producing Elegant Technical Research Reports Efficiently

(January 19 - 20, 2016)

#### **Patron**

Dr. M.S. Saini, Director

**Chairman** Co- Chairman

Dr. J.N. Jha Dr. Parminder Singh

HoD Civil Engg. HoD Computer Sci. & Engg.

**Chief Coordinator** Coordinator

Dr. Hardeep Singh Rai Er. Sukhjit Singh Sehra

#### Organised jointly by

Department of Civil Engineering

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Department of Computer Science & Engineering Guru Nanak Dev Engineering College, Ludhiana, Punjab-141006

# Objective

The objective of this two day workshop is to provide insight into structured technical reports writing, which is of utmost importance for any research project including M.Tech and Ph.D degree programmes.

The participants would be guided on the selection of tools based on the philosophy that authors should be able to focus on the content of what they are writing without going through the pain of achieving required format. The tools to be discussed are possessing superior support for automatic formatting, mathematical equations, graphics, bibliography and handling of references over the traditional tools. This workshop will enable the academicians to productively utilise their valuable time in conducting research rather than spending time in formatting their reports/thesis. The workshop will focus on:

- 1. Introduction about tools
- 2. Basic Document Creation and Formatting the Document
- 3. Handling Graphics.
- 4. Handling References

# Who can apply?

The workshop is designed for the M.Tech students, academicians, research scholars, faculty members, and professionals, who are keen to learn innovative and time saving tools suitable for producing research reports.

# Workshop Venue, Dates and Timings

Venue: Seminar Hall, Consultancy Cell (http://bit.do/tccSeminarHall)

Dates: January 19 - 20, 2016

Timings: 09:00 a.m. to 05:00 p.m.

## **Resource Persons**

Resource persons to conduct the workshop are in-house expert faculty members with excellent track record of accomplishment in technical education, research and out-reach.

# **Boarding and Lodging**

Accommodation is available in the College Hostels and Guest House on reasonable charges on prior request.

# **Registration Fee**

The registration fee is Rs. 1,000/- for teachers, PG students and Ph.D. scholars from Academic Institutes and Rs. 1,500/- for participants from Industry and other organisations. The applicants are required to submit their application along with the registration fee in the form of a DD, drawn in favour of "Director, Guru Nanak Dev Engineering College", payable at Ludhiana.

# How to register for the workshop?

Please visit http://bit.do/qigne for electronically fillable registration form and email the filled form to qi@gndec.ac.in along with scanned copy of demand draft before last date of registration. The registration for limited seats will be done on first-come-first basis up-to 15<sup>th</sup> January, 2016.

## How to reach?

The college is located at a distance of 5 km from Bus Stand, 6 km from Railway Station, and 115 km from Chandigarh Airport. There is availability of all sorts of public and private transports from stated places. The nearest landmark to the college is fly-over on Gill Road along Sidhwan Canal.

### **About Institute**

Guru Nanak Dev Engineering College (GNDEC), Ludhiana (established in 1956), is one of the oldest and a premier Engineering Institute of India. The Institute is set up on 88 acres of sprawling pristine land along SH 11, i.e. Gill Road (Ludhiana-Malerkotla Highway). The foundation stone of the college was laid by Honourable Dr. Rajendra Prasad Ji, President of India on April 8, 1956. GNDEC is now an autonomous college under UGC Act 1956 [2(f) and 12(B)]. The Institution has seven Under-Graduate (UG) programmes, thirteen Post-Graduate (PG) programmes, besides being a QIP centre for Ph.D. All the undergraduate courses of the Institute are accredited by National Board of Accreditation (NBA), AICTE, New Delhi.

# **About Department**

## **Civil Engineering:**

Civil Engineering Department has been accredited by National Board of Accreditation (NBA) since 2004 and is running three full time PG programmes (Structural Engineering; Geo-technical Engineering; Environmental Science and Engineering) and one part-time PG programme (Soil Mechanics and Foundation Engineering) along with UG programme in Civil Engineering in two shifts. The department has the state of art facilities for R&D activities.

## Computer Science & Engineering:

Computer Science & Engineering Department was established in 1997 and has been accredited by National Board of Accreditation (NBA). Department is currently offering UG and PG programmes with an intake of 180 and 18 respectively. The department has various laboratories equipped with state-of-the-art computing facilities to support the research and teaching activities.

# **Address for Correspondence**

Er. Sukhjit Singh Sehra

Department of Computer Science & Engineering

Guru Nanak Dev Engineering College

Ludhiana, Punjab - 141006

Phone: +91 98559 59200 Email: qi@gndec.ac.in

#### ABOUT THE INSTITUTE

Guru Nanak Dev Engineering College, Ludhiana is one of the oldest and premier Engineering Institutes of India. It is an autonomous college under UGC Act 1956 [2(f) and 12(B)]. The Institute is setup on 88 acres of sprawling pristine land on Ludhiana-Malerkotla state highway. Ludhiana city is a well known Industrial hub in Northern India and is well connected by direct rail service with all parts of India. The beautiful campus is about 7 kms away from railway station.

#### PROGRAMME OBJECTIVES

Innovations in optical fiber technology are revolutionizing the world communication by allowing direct transmission of high-speed signals over long distances. Optical fiber communication (OFC) has found numerous applications in Telecommunications, Local area networks, High precision material processing, Sensors, Medicine etc. It is one of the fastest growing technological markets because of relatively compact size of fiber, cost-effective, high efficiency and their stable operation.

The objective of the programme is to provide an opportunity to the participants to learn and appreciate the technology options and developments taken place during past decades in the field of OFC targeting the recent trends in telecommunication. Special emphasis will be on practical aspects of optical fiber performance and testing techniques.

#### **COURSE CONTENTS**

Following topics related to fundamentals and advances in OFC Technology will be covered:

- Needs for Optical Transmission;
- Fundamentals of Optical Wave Guiding;
- Transmission Characteristics/ Signal Degradation;
- Fabrication, Cabling and Joints;
- Optical Sources and Detectors;
- WDM/DWDM Technique & Components;
- Non Linear Effects in Optical fibers;
- PON and ROF
- Hands on session

#### **FACULTY**

Experts from NITTTR, NITs, and other renowned academic institutions around the region besides faculty from our institute will deliver expert talks.

#### **ELIGIBILITY CRITERIA**

The program is interdisciplinary in nature and has been designed for Electronics, Electrical, I&C, Computer Science and Applied Physics faculty and practicing engineers.

#### IMPORTANT DATES

Last date for receiving Applications Form: April 18, 2016

Notification of Acceptance: April 19, 2016

#### REGISTRATION

There is no registration fee for the participants from academic Institutions (AICTE approved). However, participants from industry shall have to pay registration fee of Rs. 5000/-plus ST @ 14.5% (Total Rs. 5725/-) as course fee.

#### CONTACT DETAILS

#### .Dr. Baljeet Kaur

Assistant Professor Department of ECE, GNDEC, Ludhiana Phone: 0161-5064765 (O)

E-Mail: baljeetkaur@gndec.ac.in

#### Dr. B.C. Choudhary

Professor & Head Applied Science Department, NITTTR, Chandigarh. Phone: 0172-2759556 (O) E-Mail: bakhshish@yahoo.com

#### ABOUT NITTTR

National Institute of Technical Teachers Training and Research (NITTTR) Chandigarh was established in 1967 by MHRD, GOI for promoting excellence in Technical Education through Innovations. The institute is committed to provide high quality and customized education, training, research and development and extension services to technical education. vocational industry and community.

# Short Term Course **on**

#### ADVANCES IN OFC TECHNOLOGY

April 25 - 29, 2016

In Association with NITTTR, Chandigarh



#### **Chief Patron**

Dr. M.S. SAINI

#### **Patron**

Dr. SANDEEP SINGH GILL

#### **Coordinators**

Dr. B.C. CHOUDHARY Dr. BALJEET KAUR

Organized by



Department of Electronics and Communication Engineering

GURU NANAK DEV ENGINEERING COLLEGE Ludhiana, Punjab -141006 Phone No.-0161-5064765 Website: www.gndec.ac

#### NATIONAL INSTITUTE OF TECHNICAL TEACHERS' TRAINING AND RESEARCH SECTOR 26, CHANDIGARH – 160 019 Ph.0172-2791500, Fax: 0172-2791366

#### **Short Term Course on**

#### **Advances in OFC Technology**

25<sup>th</sup> April –29<sup>th</sup> April, 2016

#### **APPLICATION FORM**

1. Name (in BLOCK letters):			
2. Educational Qualification:			
3. Designation, Department and Ir	stitute:		
4. Address for Correspondence (C	Official):		
_			
Phone:	Fax:		
E- Mail (if any):			<del></del>
5. Whether the Institute is:	☐ Govt	☐ Govt Aided	
(Please tick appropriate box)	☐ Private	☐ Self-financing	
6. Experience (if any): in Production/Development/ Tr			
7. Course fee, if applicable: Amou	nt:	_ DD No	
		Signa	ature of the Applicant
Recommended/Forwarded by Date:		· · · · · · · · · · · · · · · · · · ·	gnature of Head of ion (with office seal)

#### CONTACT

#### Dr. Munish Rattan

Assistant Professor Department of Electronics & Communication Engg. , GNDEC, Ludhiana

Phone: 0161-5064765(O)

E-Mail:: gne.ecefdp@gmail.com

#### **Prof. Narwant Singh Grewal**

Assistant Professor
Department of Electronics &
Communication Engg., GNDEC,
Ludhiana

Phone: 0161-5064765(O)

E-Mail:: gne.ecefdp@gmail.com

#### **GENERAL INFORMATION**

Nanak Dev Engineering College, Guru Ludhiana is the oldest and a premier Engineering Institute of India. It is an autonomous college under UGC Act 1956 [2(f) and 12(B)]. The Institute is setup on 88 acres of sprawling pristine land on Ludhiana-Malerkotla state highway. Ludhiana city is a well known Industrial hub in Northern India and is well connected by direct rail service with all parts of India. The beautiful campus is about 7 kms away from railway station. The weather in the month of July in Ludhiana is hot with temperature around 42°C.

#### REGISTRATION

There is no Registration fee for the participants from academic Institutes. Participants from \* industry/ field shall have to pay registration fee of Rs. 5,000/-

\* Part Time students of GNDEC working as Professionals will be exempted from registration fee.

#### TEQIP-II Sponsored Faculty Development Programme

on

"Technology and Management for Electronic Product Design" (May 2, 2016 to May 13, 2016)



#### **Chief Coordinator**

Dr. Sandeep Singh Gill

#### **Coordinators**

Dr. Munish Rattan Prof. Narwant Singh Grewal

#### Organized by

Department of Electronics & Communication Engineering

#### GURU NANAK DEV ENGINEERING COLLEGE

Ludhiana, Punjab - 141006

Phone No. 0161-5064765 E-mail: gne.ecefdp@gmail.com

#### INTRODUCTION

With Indian manufacturing sector in general, and electronics industry in particular, set for a boom due to initiatives taken by government such as start up India, the knowledge regarding the subject of product design has become highly relevant for academicians, engineers as well as aspiring entrepreneurs. The electronic product design and management has become a very crucial process which needs to be considered for the long-term roadmap of the product in the dynamic market. The process of electronic product design requires a logical progression from a functional specification to the finished product.

The functional specifications come from the analysis of the needs and expectations of the product which can be expressed as a set of design parameters. The course aims to impact advanced knowledge and skills in the electronic product design and associated management issues.

#### **COURSE CONTENTS**

- Process of electronic product design
- Need and Formulation functional specifications, Feasibility study. Design, Prototype, Testing, Design Review, Documentation, Follow -up etc.
- Embedded systems
- Advanced controllers, Programming interfacing
- FPGA based system design
- Silicon on Chip
- Antenna On chip
- Manufacturing Technology
- CAD/CAM.
- 3-D printing
- Reliability Engineering

#### **FACULTY**

Experts from IITs, NITs and other renowned academic Institutions besides faculty from our institute will deliver expert talks.

#### **EIBILITY CRITERIA**

The program is interdisciplinary in nature and designed Engineering, all Sciences & Management Faculty, professionals, practicing Engineers, and PG students.

#### **BOARDG & LODGING**

Accommodation shall be provided in college hostels free of charge. However, guest house/ hotel/lodge may be booked on payment basis if intimated well in advance.

#### **IMPORTANT DATES**

- **Last date for Receiving Application:** 
  - April 20, 2016
- **Notification of Acceptance:**

**April 25, 2016** 

#### **TEQIP-II Sponsored FDP on**

"Technology and Management for **Electronic Product Design"** 

(May 2, 2016 to May 13, 2016)

#### **APPLICATION FORM**

Name:
Father's Name:
Date of Birth:
Academic Qualification:
Designation:
Address of Sponsoring Authority:
Experience (years) :
Mobile No
E-Mail :
Accommodation required: Yes/No
Date:

Signature of Applicant

#### SPONSORSHIP CERTIFICATE

The applicant will be permitted to participate inhe above programme, if selected.

Signature of Sponsoring authority

# REGISTRATION FORM

Research Methods for Engineers (31" May 2016 to 4" June 2016)

	Qualification
	(in Block Letters)
	Name

Institution \_\_\_\_\_\_\_Address

Designation

Mobile \_\_\_\_\_

Accommodation required:

Date

Yes / No

Signature of the Applicant

Signature & Stamp of Sponsoring Authority

# ORGANIZING COMMITTEE

Patron M.S. S

Prof. (Dr.) M.S. Saini Director

Chief Co-ordinator
Prof. (Dr.) Sehijpal Singh
Head of the Department

Coordinators

Prof. (Dr.) Harwinder Singh Prof. (Dr.) Paramjit Singh

Co-Coordinators

Prof. Ardamanbir Singh Prof. Aprinder Singh

Organizing Committee Faculty,

Deptt. of Mech. Engg.

CONTACT PERSONS

Prof. (Dr.) Harwinder Singh Mob.: +91-98151-88044 Prof. (Dr.) Paramjit Singh Mob.: +91-98159-06659

Email:

harwin75@gndec.ac.in psbilga@gndec.ac.in

# ONE WEEK FACULTY DEVELOPMENT PROGRAMME

(Under TEQIP - II)

on

# RESEARCH METHODS FOR ENGINEERS

(31" May 2016 to 4th June 2016)





Organized by Deptt. of Mechanical Engineering

**GURU NANAK DEV ENGINEERING COLLEGE** 

Ludhiana - 141 006, Punjab

#### About the GNDEC, Ludhiana.....

Guru Nanak Dev Engineering College (GNDEC) was established by the Nankana Sahib Educational Trust (NSET) in the year 1956. NSET was founded in the memory of the most sacred temple of Nankana Sahib, birth place of Guru Nanak Dev Ji. The main mission for establishing Guru Nanak Dev Engineering College was 'Removal of Economic Backwardness through Technology'. To fulfill this commitment, a unique admission criterion was adopted by NSET by reserving 70% seats for the students of rural areas. At present seven undergraduate and thirteen post graduate courses are being offered in Engineering apart from MBA and MCA. The college has been declared QIP centre for Ph.D. admissions by AICTE in Civil Engineering, Mechanical Engineering and Electrical Engineering.

#### **About the Department.....**

Civil Engineering Department has been accredited since 2004 by NBA and also by NAAC. It is running three full time PG programmes (Structural Engineering.; Geotechnical Engineering.; Environmental Science and Engineering) and one part-time PG programme (Soil Mechanics and Foundation Engineering) along with UG programme in Civil Engineering in two shifts. The department has the state of art facilities for R&D activities. The Department is actively engaged in providing consultancy services to different government and private sector in and around Punjab with an annual turnover of approximately Rs one crore. The department has been declared as QIP Centre for Ph.D by AICTE, New Delhi from the session 2011-2012 and is supported under the FIST scheme and TEQIP-II scheme GOI.

#### About the Course.....

One of the major environmental problems faced by India is management of solid waste in cities and towns. In almost all the cities/towns in the developing world, the municipal authorities are responsible for the collection, transport and disposal of municipal wastes. India lacks appropriate strategies, methods for mobilization of financial resources and the necessary infrastructure for organized waste management. This leads to low collection coverage, especially in poor or marginal areas of the cities, and also to uncontrolled disposal. In India, approximately 48 million tonnes of solid waste is generated annually and about 5-25% of the urban local bodies budget is utilized for solid waste management. Up to 50% of the waste generated daily remained uncollected in the cities; the remaining waste is collected irregularly, dumped in an uncontrolled manner, or burnt on vacant plots or on the outskirts of cities. In this course, an attempt has been made to provide a comprehensive review of municipal solid waste (MSW) management for Indian cities to evaluate the current status and identify the challenges and opportunities of MSW management.

#### Course contents.....

- Municipal Solid Waste Management
- Sources, composting and properties of solid waste
- Solid waste generation and collection rates
- Collection and transport of solid waste
- Disposal of solid waste
- Composting and anaerobic digestion of organic solid waste
- Bioremediation
- Environmental Economics and waste management
- Regulations for waste management
- Landfills operation and closure

TEQIP-II sponsored
Short Term Training Program
On

Solid Waste Management in Indian Cities - Issues and Concerns

August 23-27, 2016



Organized by

Civil Engineering Department, Guru Nanak Dev Engineering College, Ludhiana (Punjab)- 141006

In association with

**IGS: Ludhiana Chapter** 

#### How to Reach GNDEC, Ludhiana

The institute is located on Ludhiana-Malerkotla road (also called Gill road) and is around 6 km from the Bus Stand and 7 km from the railway station. Ludhiana is well connected to other parts of the country by both rail and road. Important trains, like Shatabdi Express, Rajdhani Express, Uttar-Sampark Kranti Express, Howrah Mail, Malwa Express, Golden Temple Mail, Pachim Express and Sachkhand Express halt at the Ludhiana railway station. Regular buses (both, AC and Non-AC) are available from New Delhi and Chandigarh. Nearest airport is at Chandigarh, which is around 100 km from the institute.

#### **Resource Persons**

The course will be taught by faculties/scientists from IITs, NITs, local and near-by academic institutions. Experts from the field will also share their experiences during this course.

#### **Eligibility**

A faculty of an Engineering College/University; professionals from Industries; Scientists / Research Scholars from institutions can attend the course. The course is interdisciplinary one. The number of participants will be restricted to 40 as per the TEQIP norms.

#### Accommodation

A limited number of rooms with shared accommodation are available in the guest house at Guru Nanak Dev Engineering College campus. Accommodation will be arranged on request on 'first-come first-serve basis'. All participants are requested to tick the relevant column in the registration form regarding this.

#### Registration

There is no registration fee for the participants and No TA/DA will be paid to the participants. Since the seats are limited, the registration will be done on first-come first-serve basis. The registration process will be completed only when the registration form is received from the participant through a proper channel by 17<sup>th</sup> August, 2016. However, candidates may send an advance copy through email, post or by hand.

#### **Organizing Committee**

#### Patron

Dr. M.S. Saini,
Director, Guru Nanak Dev Engineering College,
Ludhiana

#### **Advisors**

Dr K.S. Gill, Professor and Head, Dr H.S. Rai, Professor and Dean (Testing & Consultancy), Dr Harvinder Singh, Professor, Department of Civil Engineering, Guru Nanak Dev Engineering College, Ludhiana

#### Coordinator

Prof. Puneet Pal Singh Cheema, Assistant Professor, Department of Civil Engineering, Guru Nanak Dev Engineering College, Ludhiana

#### Co-coordinator

Prof. Preetinder Kaur, Assistant Professor, Department of Civil Engineering, Guru Nanak Dev Engineering College, Ludhiana

#### Correspondence

Please mail the signed copy of the registration form at the email id: ppsc@gndec.ac.in and ppsc390@gmail.com
Contact no.— 9781816320 (coordinator), 0161-5064735
The hard copy can be addressed to the Coordinator (STTP -16), Civil Engineering Department, Guru Nanak Dev Engineering College, Gill Road, Ludhiana-141006.

#### Guru Nanak Dev Engineering College, Ludhiana

TEQIP-II sponsored Short Term Training Program
On

#### Solid Waste Management in Indian Cities-Issues and Concerns

(August 23-27, 2016)

#### **REGISTRATION FORM**

#### **Sponsorship Certificate**

The applicant is hereby sponsored and will be allowed to attend this course, if selected.

Date:....

**Signature and Seal** of sponsoring authority

#### **National Workshop**

on

#### **DESIGN OF MACHINE ELEMENTS USING NASTRAN SOFTWARE**

(Under TEQIP-II)

**16<sup>th</sup> – 17<sup>th</sup> September, 2016** 



**Organized by:** Mechanical Engineering Department, Guru Nanak Dev Engineering College, Gill Road, Ludhiana - 141006 Punjab, INDIA

#### **ORGINIZING TEAM**

Patron

Dr. M. S. Saini, Director

Chairman

Dr. Sehijpal Singh, HOD (Mech.)

Coordinator

Dr. Gurinder Singh Brar, AP (Mech.)

Co-Coordinator

Er. Aprinder Singh Sandhu, AP (Mech.)

#### INTRODUCTION

The pace of change in technology has taken all of us by surprise. Due to globalization, technology is passing through a very critical phase, giving high competition for final product. Gone are the days when 2D drawings were used. In today's world 3D drawings/model have become an emerging area in the design field. This course is suitable for all those who want complete exposure of 3D modeling and finite element analysis. The course will not only help designers to learn 3D software but will also help them to understand the practical applications of the tool. Sufficient time will be devoted on building correct modeling concepts so that designers can handle any kind of modeling/analysis problem in an efficient manner.

Dr. Gurinder Singh Brar, Assistant Professor, Department of Mechanical Engineering,

Guru Nanak Dev Engineering College, Gill Road Ludhiana (Punjab) INDIA - 141006

#### **GENERAL INFORMATION**

Guru Nanak Dev Engineering College, Ludhiana (established in 1956), is one of the oldest and a premier Engineering Institute of India. The institute is set up on 88 acres of sprawling pristine land on Ludhiana-Malerkotla state highway. Ludhiana city is a well known Industrial hub in Northern India and is well connected by direct rail service with all parts of India. The beautiful campus is about 7 Kms. away from railway station. The weather in the month of September in Ludhiana is mild (28°C to 34°C).

#### HOW TO APPLY

The participant should send the attached application form or a Xerox copy of the same along with statement of 500 words mentioning how this workshop will be helpful to the participant.

#### ADDRESS FOR CORRESPONDENCE

Dr. Gurinder Singh Brar, Assistant Professor, Department of Mechanical Engineering, Guru Nanak Dev Engineering College, Ludhiana. Pin – 141006.

Mobile: 91-9781991160.

E-mail: brar\_gurinder@gndec.ac.in

#### **DEPARTMENT AND FACULTY**

The Mechanical Engineering Department is NBA accredited and runs both UG and PG courses. The department has state of art facilities for research and development. It has well established CAD and Simulation Lab and Computational Lab equipped with sufficient number of computers and latest software.

Resource persons:

Er. Abhinay, CSM Softwares, New Delhi.

Dr. Gurinder Singh Brar.

Er. Aprinder Singh Sandhu.

Er. Khushpreet Singh, Consultant, Future Tech Design, Gurgaon

#### **ELIGIBILITY CRITERIA**

Students pursuing B. Tech. (second year onwards), M. Tech. and Ph. D. in Mechanical or Production Engineering are eligible to apply for participation.

#### REGISTRATION

There will be no registration fee. As seats are limited so participants are required to send the application form and statement of purpose (500 words) to the Coordinator by 15<sup>th</sup> September, 2016. Selection will be made based on statement of purpose and profile of the candidate.

#### **National Workshop**

on

# DESIGN OF MACHINE ELEMENTS USING NASTRAN SOFTWARE (Under TEOIP-II)

**16<sup>th</sup> – 17<sup>th</sup> September, 2016** 

#### **APPLICATION FORM**

Name:
Father's Name:
Date of Birth:
Level (B.Tech./M.Tech./PhD):
Branch:
Name of College:
Mailing Address:
E-mail:
Contact No.:
Please register me for the above workshop.



#### Prof. Gurpurneet Kaur

Assistant Professor Department of Electronics & Communication Engg., GNDEC, Ludhiana.

Phone: 0161-5064765(O)

E-Mail: gne.ecefdp@gmail.com

#### Prof. Naazia Makkar

Assistant Professor Department of Electronics & Communication Engg., GNDEC, Ludhiana.

Phone: 0161-5064765(O)

E-Mail: gne.ecefdp@gmail.com

#### GENERAL IONFORMATION

Guru Nanak Dev Engineering College, Ludhiana is the oldest and a premier Engineering Institute of India. It is an autonomous college under UGC Act 1956 [2(f) and 12(B)]. The Institute is setup on 88 acres of sprawling pristine land on Ludhiana-Malerkotla state highway. Ludhiana city is a well known Industrial hub in Northern India and is well connected by direct rail service with all parts of India. The beautiful campus is about 7 kms away from railway station. The weather in the month of July in Ludhiana is hot with temperature around 25°C.

#### REGISTRATION

There is no Registration fee for the participants from academic Institutes. Participants from industry/ field shall have to pay registration fee of Rs. 1,000/-

TEQIP-II Sponsored

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#### TWO DAY WORKSHOP

on

"EFFECTIVE TEACHING PEDAGOGY & RESEARCH METHODOLOGY" (March 17, 2017 to March 18, 2017)



Chief Coordinator

#### Dr. Sandeep Singh Gill

Coordinatory

#### Dr. Munish Rattan Prof. Narwant Singh Grewal

Organising Secretary

Prof.Gurpurneet Kaur Prof. Naazia Makkar

Organized by

Department of Electronics & Communication Engineering

GURU NANAK DEV ENGINEERING COLLEGE

Ludhiana, Punjab - 141006

Phone No. 0161-5064765 E-mail: gne.eccfdp@gmail.com

#### ABOUT

Pedagogy is the art (and science) of teaching. The word teaching is a simple word but its meaning or significant is not so and the term effective teaching demands various integrated activities in teaching-learning system. Effective teachers use an array of teaching strategies because there is no single, universal approach that suits all situations.

Scientific research is a chaotic business, stumbling along amidst red herrings, errors and truly, creative insights. Research is an investigation of finding solutions to scientific and social problems through objective and systematic analysis. It is a search for knowledge, that is, a discovery of hidden truths. Only through research it is possible to make progress in a field. The results of scientific research very often force a change in the philosophical view of problems which extend far beyond the restricted domain of science itself.

The course aims at imparting skills for effective teaching pedagogy and research methodology.

#### COURSE CONTENTS

- Essentials of Effective Research.
- Effective Teaching.
- Modern Teaching Tools.
- Teaching Strategies.
- · Research definition, types & framing of research
- Research design in context of Engg. Problems.
- Hypotheses Testing

#### FACULTY

Experts from NITTR, Chandigarh and Deenbandhu Chhota Ram University of Science and Technology. Sonepat besides faculty from our institute will deliver expert talks.

#### ELIGIBILTY CRITERIA

The program is interdisciplinary in nature and has been specifically designed for all Engineering Sciences & Management PG students and newly joined Faculty members.

#### BOARDING AND LODGING

Accommodation shall be provided in college hostels free of charge. However, guest house/ basis if hotel/lodge may be booked on payment intimated well in advance.

#### IMPORTANT DATES

Last date for receiving Application:

March 15, 2017

Notification of Acceptance:

March 16, 2017

#### TEOIP-II Sponsored Workshop on

#### "EFFECTIVE TEACHING PEDAGOGY & RESEARCH METHODOLOGY

(March 17, 2017 to March 18, 2017)

Name:		pt	
Father's Name:		83	
Date of Birth:			
Academic Qualific	ation:	***	\$10° 104
Designation:			
Address of Spons	soring Autho	ority:	
Experience (years) : .		*** *** ***	***
Mobile No			
E-Mail :			
Accommodation	required:	Yes/	No
Date:	Signa	ture of A	pplicant

The applicant will be permitted to participate inhe above programme, if selected.

Signature of sponsoring authority

#### **ELIGIBILITY**

The FDP is interdisciplinary and is open to all faculty members from CSE, IT, EE, ECE, ME, Production departments and computer science applications. Research Scholars /M.Tech students and industry personnel working in the concerned areas are also eligible.

#### **HOW TO APPLY**

Applicants can fill the registration form only in **online mode** available at http://it.gndec.ac.in/fdp\_ro.

After successful registration, pdf form will be generated and applicants are required to download the form and send the form duly signed from sponsoring organization to <a href="mailto:it@gndec.ac.in">it@gndec.ac.in</a> before deadline along with scanned copy of Demand Draft/Cheque.

Last date for receipt of

**Application:** <u>06.10.2017</u>

Date of Intimation

regarding final Selection: 13.10.2017

Brochure can also be downloaded from our website: http://it.gndec.ac.in/fdp\_ro

#### **ADDRESS FOR CORRESPONDENCE**

Dr. Amit Kamra (Asstt. Prof.)
Co-Coordinator
Contact: - 98142-29753
Department of Information Technology
Guru Nanak Dev Engineering College
(An Autonomous College u/s 2(f) and 12(B) of
UGC Act, 1956)

Gill Road, Gill Park, Ludhiana-141006, Punjab. Email: - it@gndec.ac.in

#### REGISTRATION

There is no registration fee for the FDP but an amount of Rs 1000 (Refundable) in form of Cheque/DD in favour of Director GNDEC is required. The above amount will be refunded to the participant if he/she attends the FDP. In case a participant does not attend the FDP, the above amount will be forfeited. All shortlisted candidates are required to confirm their participation by submitting duly signed registration form generated online to it@gndec.ac.in

#### TRANSPORT, BOARDING & LODGING

All outstation Participants are entitled for  $2^{nd}$  class AC railway fare to and fro by the shortest route from their college to GNDEC, Ludhiana. Limited accommodation will be provided in the students Hostels or Guest House on sharing basis (subject to availability).

#### **CHIEF PATRON**

Dr. M.S. Saini

(Director, GNDEC)

#### **COORDINATORS**

Dr. K.S. Mann

Coordinator (Professor & HOD- I.T.)

Dr. Amit Kamra

Co-Coordinator (Asstt. Prof.-I.T.)

#### **AICTE Sponsored**

**Two Weeks Faculty Development** 



**Programme** 

0n

#### Research Opportunities in Medical Imaging, Biomedical Engineering and Health Informatics

OCT 23, 2017-NOV 3, 2017



Department of Information Technology Guru Nanak Dev Engineering College Ludhiana, Punjab-141006

> email: it@gndec.ac.in Contact: -98142-29753

#### **ABOUT THE INSTITUTE**

Guru Nanak Dev Engineering College (GNDEC), Ludhiana is one of the oldest and a premier Engineering Institute of India straining persistently in the field of education by comprehending the need for up-liftment of rural youth in highly competitive, technologically elevated society and understanding the thirst for knowledge of students and quenching it in general. The foundation stone of the college was laid by Honorable Dr. Rajendra Prasad Ji, President of India on 8thApril, 1956. The institution runs seven B.Tech., thirteen M.Tech., MBA and MCA FDPs, besides being a QIP centre for Ph.D.

The College is affiliated with I.K. Gujral Punjab Technical University (IKGPTU). The College FDPs are approved by All India Council for Technical Education, New Delhi.

This is the first Engineering College of Punjab, which was conferred Autonomous Status by University Grants Commission(UGC), New Delhi in 2012 under section 2(f) and 12(B) of UGC Act 1956. Institution is accredited by National Assessment and Accreditation Council (NAAC) (A Grade) and TCS. The institute is set up on 88 acres of sprawling pristine land on Ludhiana-Malerkotla highway in Punjab.

#### **ABOUT THE DEPARTMENT**

The department of Information Technology in Guru Nanak Dev Engineering College, Ludhiana was established in 2001. The mission of the Department of Information Technology is to impart high quality post graduate and undergraduate education and carry out leading-edge research in the discipline of Information Technology. The department currently runs academic program leading to the award of B.Tech. in Information Technology degree of I.K.G Punjab Technical University Jalandhar. The B.Tech (IT) program of department has been accredited twice by the by NBA.

#### ABOUT FDP

By nature, field of Medical Imaging, Biomedical Engineering and Health Informatics is an interdisciplinary endeavor. A faculty development program on Research opportunities in Medical Imaging, Biomedical Engineering and Health Informatics is aimed at introducing the possible research avenues in the field of Biomedical Engineering and will be an opportunity for the participants who are interested in interdisciplinary areas where they can apply their expertise to solve problems in medicine and health care.

#### **OBJECTIVE OF THE FDP**

- Gain Insights into the emerging trends and researches in field of Medical Imaging
- Hands on experience of data analytical tools imperative in field of bioinformatics and medical imaging
- Research trends in healthcare and medical industry

#### **FDP CONTENTS**

The contents of FDP will address all the current and upcoming issues related to areas of medical imaging, biomedical engineering and health informatics. In addition Hands on training for tools like MATLAB, WEKA, and OCTAVE will also be provided to the participants.

#### **RESOURCE PERSON**

Eminent Speakers from IIT/NIT/UIET/GNDEC and doctors from reputed Hospitals will be handling the sessions.

#### **AICTE Sponsored**

**Two Weeks Faculty Development Programme** 

on

#### Research Opportunities in Medical Imaging, Biomedical Engineering and Health Informatics

Name (in block letters):
Designation:
Department:
Cheque/DD No:
Organization:
Mailing Address:
Age: Gender (M/F):
Mobile:
Email:
Educational Qualifications:
Accommodation Required: Yes/ No
Signature of Applicant:
Sponsorship & signature of Head of the College/

Form should be complete otherwise

Institute (with date & seal)

application will be rejected.

#### **HOW TO APPLY**

Completed Registration form along with one page write up (on reasons to attend this course) should be sent by email to qipgndecme@gmail.com. Please send single email with following attachments:-

- (i) Scanned copy of the filled in and signed form, and
- (ii) One page write-up

The title of the email should be "QIP course on Finite Element Method for Engineering Applications".

The hard copy of the same should be send to following address:-

Dr. Gurinder Singh Brar (Course Coordinator)

Er. Aprinder Singh Sandhu, Er. Chatwant Singh Pandher

(Course Co-Coordinator)

Department of Mechanical Engineering, Guru Nanak Dev Engineering College, Ludhiana, Punjab. (INDIA) 141006.

#### **IMPORTANT DATES**

Deadline for submitting Applications  $-21^{st}$  Nov., 2017

Notification of Acceptance – 25<sup>th</sup> Nov., 2017

#### **VENUE FOR COURSE**

Workshop Seminar Hall, GNDEC, Ludhiana

#### **ELIGIBILITY**

Industry Persons, Research Scholars and Faculty members of degree level engineering colleges recognized by AICTE are eligible to attend the course.

#### REGISTRATION

There is no registration fee for the course. All short-listed candidates are required to confirm their participation by sending a cheque/Demand draft of Rs. 1000/- in the name of "Principal, GNDEC, Ludhiana".

The above amount will be refunded to the participant if he/she attends the course. In case a participant does not attend the course, the above amount will be forfeited. Shortlisting and confirmation of eligible candidates will be on a first come first served basis up to a maximum of 30 candidates.

#### TRAVEL AND ACCOMMODATION

Participants are entitled for II class (Sleeper Class) or AC railway fare to and fro by the shortest route from the college to GNDEC, Ludhiana. Accommodation will be provided in Guest House or Student Hostels on sharing basis.

#### **FACULTY COORDINATOR**

Dr. Sehijpal Singh Khangura

#### **COURSE COORDINATOR**

Dr. Gurinder Singh Brar

#### **COURSE CO-COORDINATOR**

Er. Aprinder Singh Sandhu, Er. Chatwant Singh Pandher

#### **ORGANIZING COMMITTEE**

Faculty of Mechanical Engineering

#### **GUEST FACULTY**

Dr. Inderdeep Singh (IIT, Roorkee), Dr. J. S. Saini (TU, Patiala), Dr. Indra Vir Singh (IIT, Roorkee)

#### **QIP SHORT TERM COURSE**

ON

# FINITE ELEMENT METHOD FOR ENGINEERING APPLICATIONS

(4<sup>th</sup> – 8<sup>th</sup> December, 2017)



Department of Mechanical Engineering,
Guru Nanak Dev Engineering College,
Ludhiana – 141006
Punjab, INDIA
Mobile - +91-9781991160,
+91-9914701236,
+91-9478230013.
Email – qipgndecme@gmail.com
www.gndec.ac.in

#### ABOUT THE INSTITUTE

Guru Nanak Dev Engineering College, Ludhiana (established in 1956), is one of the oldest and a premier Engineering Institute of India. The foundation stone of the college was laid by Honorable Dr. Rajendra Prasad Ji, President of India on 8<sup>th</sup> April, 1956. GNDEC is now an autonomous college under UGC act. The ISO-9001:2000 certified institution has seven B.Tech. courses, thirteen AICTE approved postgraduate courses besides being a QIP centre for PhD.

The institute is set up on 88 acres of sprawling pristine land on Ludhiana-Malerkotla state highway and is 7km from the railway station, 6km from Main Bus Stand and 15km from the Airport. Ludhiana is well connected by roads and is located at 300 km from New Delhi, 90 km from Chandigarh, 150 km from Amritsar.

#### ABOUT THE DEPARTMENT

Mechanical Engineering Department was established in 1957 and is currently offering B.Tech. (Mech. Engg.), M.Tech. (Prod. Engg.), M.Tech. (Ind. Engg.) and PhD courses. Since its inception, the department has contributed in a big way to the society by providing technical manpower for various fields in India and abroad. Around 3000 graduates and 300 post graduates from the department are settled in very good positions all over the Globe. The faculty of the department continues to provide new frontiers of technical knowledge to the students by imparting quality education and

research. Presently, Dr. Sehijpal Singh Khangura is Head of Department.

#### COURSE CONTENTS

- 1. Basics and Fundamentals
- 2. Approaches to solve FE problems
- 3. FEM for beams, plates and axisymmetric problems
- 4. FEM for solid mechanics and fluid flow problems
- 5. Demonstration of FE software
- 6. Case Studies

#### **OBJECTIVES OF THE COURSE**

Finite element analysis has become an integral part of design for automotive, aerospace, civil construction, consumer and industrial goods industry. The objectives of this course are:

- (a)To develop the fundamental understanding of the principle of finite element method.
- (b)To introduce finite element method (FEM) to participants so that they are equipped to solve various problems of their field.
- (c)To familiarize the participants to FE software to carry out various types of FE analysis.
- (d)To apply FEM to solve problems in different engineering disciplines namely stress analysis, fluid flow, heat transfer etc.

#### RELATED DISCIPLINES

Mechanical, Production and Civil Engineering.

#### Guru Nanak Dev Engineering College, Ludhiana, Punjab-141006

QIP Short Term Course on

#### FINITE ELEMENT METHOD FOR ENGINEERING APPLICATIONS (4<sup>th</sup> - 8<sup>th</sup> December, 2017)

#### **APPLICATION FORM**

l.	Name:
2.	Educational Qualification:
3.	Designation:
1.	Institute:
5.	Address for Correspondence:
	Phone:
	E-mail:
5.	Experience:
7.	Accommodation Required: YES/NO
)a	te: Signature of the Applicant

Signature of Head of the College/Institute (with date & seal)

(Important: By signing above head of the College/Institute certifies that applicant is a faculty member of degree level engineering college recognized by AICTE)

\*Form should be complete otherwise the application will be rejected

#### **COURSE OBJECTIVE**

Low power has emerged as a principal theme in today's electronics industry. With the remarkable success and growth of the class of personal computing devices (portable desktops, audio- and video-based multimedia products) and wireless communications systems (personal digital assistants and personal communicators), demand of high-speed computation and complex functionality with low power consumption has increased. In the past, the major concerns of the VLSI designer were area, performance, cost and reliability; power consideration was mostly of only secondary importance. In recent years, however, this has begun to change and power is being given comparable weight along with area and speed considerations. Thus, the need for low power has caused a major paradigm shift where power dissipation has become as important a consideration as performance and area.

Many issues are faced by designers at architectural, logic, circuit and device levels which are required to be overcome by efficient techniques. This course will address the latest trends and future challenges that must be met to design low power, high performance systems.

#### **COURSE CONTENTS**

- **❖** VLSI Design cycle
- Challenges at various design levels
- **❖** Power Estimation Methods
- Power reduction techniques at each design level
- **\*** Effects of scaling
- \* CMOS Analog design
- **❖** Digital circuit design
- CAD Techniques
- Nanoscale Semiconductor Devices
- Hands-on session on tools like Cadence and TCAD.
- **\*** Future Challenges

#### IMPORTANT DATES

Submission of Registration form by 01.12.2017 Acceptance Notification by 02.12.2017

#### DATE & VENUE FOR REGISTRATION

4.12.2017 (9AM) at TCC Seminar Hall, GNDEC, Ludhiana

#### **CONTACT DETAILS**

Dr. Sandeep Singh Gill
Professor & Head,
Department of Electronics &
Communication Engineering,
Guru Nanak Dev Engineering College,
Gill Road, Gill Park, Ludhiana,
Punjab – 141006
E-mail:gndec.ece.fdp@gmail.com

Phone: 0161-5064576(O)

#### **PATRON**

Principal, Guru Nanak Dev Engineering College, Ludhiana

#### **COORDINATOR**

Dr. Sandeep Singh Gill

#### **ORGANISING COMMITTEE**

Pf. Narwant Singh
Dr. Balwinder Singh
Dr. Munish Rattan
Dr. Baljeet Kaur
Pf. Harminder Kaur
Pf. Gurjot Kaur
Pf. Navneet Kaur
Pf. Gurpurneet Kaur
Pf. Tarandeep Singh

Pf. Chahat Jain

Pf. Kuldeepak Singh

Pf. Harminder Kaur Aulakh

#### TWO WEEK AICTE SPONSORED

#### **FACULTY DEVELOPMENT PROGRAMME**

ON

#### "Design Challenges in Low Power VLSI Design"

04.12.2017 to 16.12.2017





### Coordinator Dr. Sandeep Singh Gill

#### Organized by

Department of Electronics and Communication
Engineering

# Guru Nanak Dev Engineering College Ludhiana

Gill Road, Gill Park, Ludhiana, Punjab – 141006 E-mail: gndec.ece.fdp@gmail.com Phone: 0161-5064576(0)

#### ABOUT THE INSTITUTE

Guru Nanak Dev Engineering College (An Autonomous College under UGC Act), established in 1956, has been striving persistently in the field of education by comprehending the need for upliftment of rural youth in highly competitive, technologically-elevated society, in particular, and understanding the thirst for knowledge of students and quenching it in general. The College has been declared an Autonomous College by UGC, New Delhi on 17.8.2012. This college has the privilege of starting Ph.D degree under Quality Improvement Programme (QIP) by AICTE, New Delhi. Apart from offering various accredited graduate level B.Tech courses, the institute also imparts thirteen Postgraduate courses both on regular and part time basis. An excellent platform is provided to the researchers leading to Ph.D degree program.

#### ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering, established in 1981, is one of the most dynamic departments of Guru Nanak Dev Engineering College. It was the first most diversification initiative by the college, 25 years after its establishment. The department is currently running NBA Accredited UG and PG courses in Electronics and Communication Engineering and has around 15 scholars enrolled for doctorate in areas like Antenna Design, VLSI, Optical communication etc. Ever since its inception, the department has been the hub of academic excellence through some great teachers who have spread their wings all over the globe. The alumni of the department are not only excelling in India but also in the Silicon Valley and other hubs of Electronics Technology. They are at the forefront of the Telecom revolution of the last 20 years and manning pivotal positions in Telecom operators in India, South East Asia, Pacific region and Africa. In the last two years, as a part of an academically autonomous system, the department has been taken numerous quality initiative including revamping of syllabi so that it is in tune with the outcome based system as proposed in the Washington Accord. The

department is having very good pass percentage usually above 95% for UG/PG students. ECE Department is continuously maintaining strong International collaboration with outside world in form of joint research publications, editorial board memberships of reputed International journals.

#### REGISTRATION

There is no registration fee for participants from academic institutes. All short-listed candidates are required to confirm their participation by sending the scanned copy of duly filled Registration form by Email to <code>gndec.ece.fdp@gmail.com.</code>

Shortlisting and confirmation of eligible candidates will be on a first come first served basis. Incomplete application forms will not be entertained. Registration forms should reach the coordinator by 27.11.2017.

#### **ELIGIBILITY**

Faculty members of degree level engineering colleges recognized by AICTE are eligible to attend the course. Applications from various fields like ECE/EE/CSE/IT and other related disciplines are invited.

<u>Course Completion Certificate will be given if</u> <u>the attendance criterion is satisfied.</u>

#### TRANSPORT, BOARDING & LODGING

Participants are entitled for II class (Sleeper Class) or AC railway fare to and fro by the shortest route from college to GNDEC, Ludhiana. Accommodation will be provided in the students Hostels or Guest House on sharing basis.

#### TWO WEEK AICTE SPONSORED

#### **FACULTY DEVELOPMENT PROGRAMME**

ON

# "Design Challenges in Low Power VLSI Design"

#### Registration Form

Name (in block letters):

Gender:
Qualification:
Designation/Department:
Organization:
Address:
Mobile:
Email :
Accommodation in Campus: YES / NO

Sponsorship & Signature of Head of the College / Institute (with date & seal).

(Important: By signing above, head of the College/Institute certifies that applicant is a faculty member of degree level engineering college recognized by AICTE)

# Waste management by three dimensional / four dimensional printing

.....

#### Overview

In today's highly competitive environment, management of physical waste (their selection, maintenance, inspection and re-usability) plays a key role in determining operational performance and profitability of any manufacturing plant or industry. Waste management, by three dimensional/ four dimensional printing being the novel way of optimizing these processes, attempts to minimize the waste on our planet and directly or indirectly influences manufacturing/production/operation/service cost, processes and quality, and throughput. There is particular interest in the application of waste management by applying principles of three dimensional and four dimensional printing to the management of engineering systems in any industrial unit where the cost, life cycle and performance of the functional prototypes are of major significance. Waste management for any engineering system needs to focus on maintenance, renewal and enhancement activities, with an integrating mechanism, on delivering sustainable outputs valued by customers and funding providers at the lowest whole-life cost emphasizing on creating knowledge of how waste material degrade and fail to optimize maintenance and renewal interventions. It is essential that industries across India, many organizations of which being environment conscious, promote a consistent waste management approach to their systems in overall manufacturing, production and supply chain domain to develop their novel methods, standards and framework for achieving excellence in reducing the waste.

This course is organized in the following manner:

- (i.) Exposing participants to the fundamentals of three dimensional and four dimensional printing for waste management,
- (ii.) Building in confidence and capability amongst the participants in the application of three dimensional and four dimensional printing and problems in terms of mechanical and structural framework,
- (iii.) Providing exposure to practical problems and their solutions, through case studies and live projects in three dimensional and four dimensional printing for waste management,
- (iv.) Enhancing the capability of the participants to identify, control three dimensional/ four dimensional printing-related problems in engineering system.

Course participants will learn these topics through lectures and hands-on experiments. Also case studies and assignments will be shared to stimulate research motivation of participants.

Modules	Waste management by 3D/4D printing : Dec. 18 – Dec 22  Number of participants for the course will be limited to Thirty.
You Should Attend If	<ul> <li>you are manufacturing/mechanical engineer or research scientist interested in fundamentals of three dimensional and four dimensional printing.</li> <li>you are civil or non-destructive test engineer interested to learn application of 3D/4D printing in your profession.</li> <li>you are a student or faculty from academic institution interested in learning how to do research on 3D/4D system or subsystem or want to work with additive manufacturing technologies.</li> </ul>
Fees	The participation fees for taking the course is as follows:  Participants from abroad: US \$500  Industry/ Research Organizations: 10,000  Academic Institutions (Faculty/UG/PG/Ph.D students): Free  The above fee include all instructional materials, computer use for tutorials and assignments, laboratory equipment usage charges, 24 hr free internet facility. The participants will be provided with accommodation/food on payment basis.

#### The Faculty



Fernando Fraternali is Professor of Structural Mechanics at the University of Salerno, Italy. Most of his research work concerns computational mechanics; multi-scale numerical modeling and simulation of materials and structures; and the computational design and engineering of innovative materials, such as highly nonlinear phononic crystals, environmentally compatible composite materials, nano materials and biomaterials.



**Rupinder Singh** is Professor of Production Engineering at Guru Nanak Dev Engineering College, Ludhiana, India. His research interest is development of 3D and 4D materials for additive manufacturing applications.

#### Course Co-ordinator

**Prof. Rupinder Singh** Phone: 098722 57575

E-mail: rupindersingh78@yahoo.com

http://www.gian.iitkgp.ac.in/GREGN

#### **COURSE OBJECTIVE**

Low Power has emerged as a Principal theme in today's electronics industry. With the remarkable success and growth of the class of personal computing devices (portable desktops, audio- and video-based multimedia products) and wireless communications systems (personal digital assistants and personal communicators), demand of high-speed computation and complex functionality with low power consumption has increased. In the past, the major concerns of the designer were area, performance, cost and reliability; power consideration was mostly of only secondary importance. In recent years, however, this has begun to change and power is being given comparable weight along with area and speed considerations. Thus, the need for low power has caused a major paradigm shift where power dissipation has become as important a consideration as performance and area.

Many issues are faced by designers at architectural, logic, circuit and device levels which are required to be overcome by efficient techniques. This course will address the latest trends and future challenges that must be met to design low power, high performance electronic systems.

#### **COURSE CONTENTS**

- ♦ Low power high performance electronic systems
- Design and Analysis of Antennas for low power applications
- ♦ Low power VLSI circuits & systems
- Low power electronics for Signal & Image Processing
- Wireless & Optical communication for low power applications
- Hands-on sessions on relevant tools

#### **IMPORTANT DATES**

Submission of Registration form by 05.03.2018

Acceptance Notification by 07.03.2018

#### **DATE & VENUE FOR REGISTRATION**

12.03.2018 (9AM) at Testing & Consultancy Cell (TCC) Seminar Hall, GNDEC, Ludhiana

#### **CONTACT DETAILS**

Dr. Sandeep Singh Gill

Professor & Head

Department of Electronics & Communication Engg.

Guru Nanak Dev Engineering College Gill Road, Gill Park, Ludhiana,

Punjab - 141006

E-mail:qipece@gmail.com

Phone: 0161-5064576(O)

#### **PATRON**

Dr. Sehijpal Singh (Principal)
Guru Nanak Dev Engineering College, Ldh.

#### COORDINATOR

Dr. Sandeep Singh Gill (HOD ECE)

#### ORGANISING COMMITTEE

Prof. Ameeta Seehra Prof. Narwant Singh

Dr. Balwinder Singh

Dr. Munish Rattan

Dr. Baljeet Kaur Prof. Harminder Kaur

Prof. Gurjot Kaur Prof. Navneet Kaur

Prof. Gurpurneet Kaur

Prof. Chahat Jain

Prof. Kuldeepak Singh

Prof. Harminder Kaur Aulakh

Prof. Jasmeet Kaur

Prof. Daljit Singh

Prof. Sapanjot Kaur

Prof. Simranjit Kaur Prof. Naazia Makkar

Prof. Nishu Kansal

# ONE WEEK QIP SHORT TERM COURSE ON



# Lon Poner Applications in Electronics"

(12<sup>th</sup>-16<sup>th</sup> March, 2018)



COORDINATOR

Dr. Sandeep Singh Gill

**ORGANISED BY** 

#### **Department of**

# ELECTRONICS & COMMUNICATION ENGINEERING

#### **Guru Nanak Dev Engineering College**

Gill Road, Ludhiana, Punjab-141006

Phone : 0161- 5064576 (O) E-mail: qipece@gmail.com

#### **ABOUT THE INSTITUTE**

Guru Nanak Dev Engineering College (An Autonomous College under UGC Act), established in 1956, has been striving persistently in the field of education by comprehending the need for upliftment of rural youth in highly competitive, technologically-elevated society, in particular, and understanding the thirst for knowledge of students and quenching it in general. The College has been declared an Autonomous College by UGC, New Delhi on 17.8.2012. This college has the privilege of starting Ph.D degree under Quality Improvement Programme (QIP) by AICTE, New Delhi. Apart from offering various accredited graduate level B.Tech courses, the institute also imparts thirteen Postgraduate courses both on regular and part time basis. An excellent platform is provided to the researchers leading to Ph.D degree program.

#### **ABOUT THE DEPARTMENT**

The Department of Electronics and Communication Engineering, established in 1981, is one of the most dynamic departments of Guru Nanak Dev Engineering College. It was the first diversification initiative by the college, 25 years after its establishment. The department is currently running NBA Accredited UG and PG courses in Electronics and Communication Engineering and has around 15 scholars enrolled for doctorate in areas like Antenna Design, VLSI, Optical communication etc. Ever since its inception, the department has been the hub of academic excellence through some great teachers who have spread their wings all over the globe. The alumni of the department are not only excelling in India but also in the Silicon Valley and other hubs of Electronics Technology. They are at the forefront of the Telecom revolution of the last 20 years and manning pivotal positions in Telecom operators in India, South East Asia, Pacific region and Africa. In the last five years, as a part of an academically autonomous system, the department

has taken numerous quality initiatives including revamping of syllabi so that it is in tune with the outcome based system as proposed in the Washington Accord. Both its courses viz. B.Tech (ECE) & M.Tech (ECE) are accredited by NBA under Tier-I. The department is having very good pass percentage usually above 95% for UG/PG students. ECE Department is continuously maintaining strong International collaboration with outside world in form of joint research publications, editorial board memberships of reputed International journals.

#### **REGISTRATION**

There is no registration fee for participants from academic institutes. All short-listed candidates are required to confirm their participation by sending the scanned copy of duly filled Registration form by Email to <a href="mailto:qipece@gmail.com">qipece@gmail.com</a>

Shortlisting and confirmation of eligible candidates will be on a first come first served basis. Incomplete application forms will not be entertained. Registration forms should reach the coordinator by 05.03.2018.

#### **ELIGIBILITY**

Faculty members of degree level engineering colleges recognized by AICTE are eligible to attend the course. Applications from various fields of engineering and other related disciplines are

#### TRANSPORT BOARDING & LODGING

Participants are entitled for II class (Sleeper Class) or AC railway fare to and fro by the shortest route from college to GNDEC, Ludhiana. Accommodation will be provided in the students Hostels on sharing basis.

# ONE WEEK QIP SHORT TERM COURSE ON

#### "Design Issues in Low Power Applications in Electronics"

(12<sup>th</sup>-16<sup>th</sup> March, 2018)

#### **REGISTRATION FORM**

Name (in block letters):	
Gender:	
Qualification:	
Designation/Department:	
Organization:	
Address:	
Mobile:	
Email :	
Accommodation Required in Campus: YES / NO	

Signature of Participant

Sponsorship & Signature of Head of the College / Institute (with date & seal).

(Important: By signing above, head of the College/Institute certifies that applicant is a faculty member of degree level engineering college recognized by AICTE)

Completed Registration form along with 1-page write-up (on reasons to attend this course) should be sent by Email to <a href="mailto:singhrupinder@gndec.ac.in">singhrupinder@gndec.ac.in</a>

Dr. Rupinder Singh Course Coordinator

#### Pf. Manjot Singh Bedi Course Co-Coordinator

Department of Production Engineering Guru Nanak Dev Engineering College, Ludhiana, Punjab, 141006

Please send single email with attachments as:

(a) the 1-page write-up and (b) scanned copy of the filled-in and signed form. The title of the email should be "QIP course on Tribological analysis of four dimensional printed engineering materials".

#### **IMPORTANT DATES**

Deadline for submitting Application: 14 Nov., 2018

(Scanned copy via Email only)

Notification of Acceptance: 15 Nov., 2018 Course Dates: 19 – 24 Nov., 2018

• Incomplete application forms will not be entertained.

#### **VENUE FOR COURSE**

Seminar Hall, Central Workshop, GNDEC, Ludhiana.

#### **DATE & TIME FOR REGISTRATION**

Nov. 19, 2018, 9 AM at the venue for the course.

#### **ELIGIBILITY**

Faculty members (Engg. discipline) of degree level engineering colleges recognized by AICTE/UGC/MHRD are eligible to attend the course. Research scholars and Industry/ R&D personal will also be considered subject to seats availability.

#### **COURSE EVALUATION**

Successful participants would be awarded 'Course Completion Certificate'.

#### **REGISTRATION**

There is no registration fee for the course. All short-listed candidates are required to confirm their participation by sending a Cheque/Demand Draft of Rs. 1000/- in the name of "PRINCIPAL, GNDEC, Ludhiana".

The above amount will be refunded to the participant if he/she attends the course. In case a participant does not attend the course, the above amount will be forfeited. Shortlisting and confirmation of eligible candidates will be on a first come first served basis up to a maximum of 30 candidates.

#### TRANSPORT, BOARDING & LODGING

Participants are entitled for II class (Sleeper Class) or AC railway fare to and fro by the shortest route from college to GNDEC, Ludhiana. Accommodation will be provided in the students Hostels or Guest House on sharing basis.

#### **CHIEF PATRON**

Dr. Sehijpal Singh

#### **FACULTY COORDINATOR**

Dr. Jasmaninder Singh Grewal

#### **COURSE COORDINATOR**

Dr. Rupinder Singh

#### **COURSE Co-COORDINATOR**

Pf. Manjot Singh Bedi

#### **GUEST FACULTY**

- Dr. O.P.Singh (BCET Gurdaspur)
- Dr. Sandeep Grover (YMCA Fbd.)
- Dr. Buta Singh Sidhu (MRRSPTU Bathinda)
- Dr. Harpreet Singh (IIT Ropar)
- Dr. Dilbag Singh (BCET Gurdaspur)
- Dr. Sanjeev Kumar (PEC Chandigarh)
- Dr. IPS Ahuja (Pbi. Univ. Patiala)
- Dr. G.S.Brar (NIT, U.K)
- Dr. Ajay Batish (TIET, Patiala)
- Dr. Dixit Garg (NIT, KKR)
  - Dr. J.S.Dureja (Pbi. Univ. Patiala)

#### **QIP Short Term Course**

On

#### TRIBOLOGICAL ANALYSIS OF FOUR DIMENSIONAL PRINTED ENGINEERING MATERIALS

Nov. 19 – 24, 2018



Department of Production Engineering Guru Nanak Dev Engineering College Ludhiana, Punjab – 141006

e-mail: singhrupinder@gndec.ac.in Phone: +91 98722-57575, +91 98035-66002

### **ABOUT THE INSTITUTE**

Guru Nanak Dev Engineering College (An Autonomous College under UGC Act), established in 1956,has been straining persistently in the field of education by comprehending the need for up-liftment of rural youth in highly competitive, technologically-elevated society, in particular, and understanding the thirst for knowledge of students and quenching it in general. The College has been declared an Autonomous College by UGC, New Delhi on 17.8.2012. This college has the privilege of starting Ph.D degree under Quality Improvement Programme (QIP) by AICTE, New Delhi. Apart from offering various Accredited graduate level B.Tech courses vis-a-vis Civil Engineering, Computer Science & Engineering, Electrical Engineering, Electronics & Communication Engineering, Mechanical Production Engineering, Information Engineering, Technology, the institute imparts instruction in eleven Postgraduate M.Tech courses both on regular and part time basis. An excellent platform is provided to the researchers leading to Ph.D degree program.

### **ABOUT THE DEPARTMENT**

The first batch of M.Tech. (Production Engineering) started as part-time programme in 1980 followed by M.Tech. (Production Engineering) full-time programme in 1983 under Mechanical Engineering department. The department was renamed as Department of Production Engineering on January, 2009 and is functioning independently. At present it offers undergraduate and postgraduate courses in Production Engineering. The department has laboratory and workshop facilities with modern sophisticated equipment to carry out research in all areas related to Production Engineering. The faculty actively participates in sponsored research and consultancy work. The PG Prod. Engg. has been accredited by NBA (Tier-I) and all the courses offered by department are AICTE approved. The department is having very good pass percentage (in stipulated time) usually above 95% for UG/PG students. Since 2009, as independent department, Production Engineering is continuously maintaining strong International collaboration with outside world in form of joint research publications, editorial board memberships of reputed International journals.

### **OBJECTIVE OF THE COURSE**

- Tribological analysis of four dimensional printed thermoplastic composites
- Application of optimization techniques for four dimensional printed engineering materials
- Hand on experience for preparation of bio medical implants of thermoplastic composites by additive manufacturing
- Lab Testing of protective coatings, tribological analysis as per ASTM standards

### **COURSE CONTENTS**

- 1. Role of optimization for biomedical implants
- 2. Four dimensional printing of thermoplastic composites
- 3. Biomedical/Protective coatings and tribological analysis
- 4. Design of experiment (various tools for process optimization)
- 5. Case studies on dental/ortho implants.
- 6. Hand on experience for lab testing of implants.

### **COURSE BENEFICIARIES**

Faculty members pursing Ph.D/ Research Scholars

### **BROAD SUBJECT AREA**

Manufacturing, Additive manufacturing

### RELATED DISCIPLINES

Production/Manufacturing/Mechanical/ Bio-medical/ Bio-technology/ CSE/IT

**NOTE:** For additional copies of the registration form, use a photocopy of this brochure or type in the format given. For further details:

http://www.pe.gndec.ac.in www.rupindersinghresearch.com **QIP Short Term Course** 

on

### TRIBOLOGICAL ANALYSIS OF FOUR DIMENSIONAL PRINTED ENGINEERING MATERIALS

November 19 – 24, 2018

### **Registration Form**

Name (in block letters):
Designation :
Organization :
Mailing Address :
Age : Sex ( M/F):
Mobile :
Email :
Educational Qualifications:
Prior Exposure to such program: Yes / No
Accommodation in Campus: YES / NO
Signature of Applicant:
Sponsorship & Signature of Head of the College

(Important: By signing above head of the College/Institute certifies

that applicant is a faculty member of degree level engineering college

Form should be complete otherwise the application will be rejected

/ Institute (with date & seal).

recognized by AICTE)

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## ONTACT DETAILS Balwinder Singh Dhallwal

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of rural youth in highly competitive, technologically-elevated society, in particular, and understanding the thirst for knowledge of students and quenching it in general. The college has been declared an autonomous college by UGC, New Delhi on 17.8.2012. This college has the privilege of starting Ph.D degree under Quality Improvement Programme (QIP) by AICTE, New Delhi. Apart from offering various B.Tech courses, the institute also imparts thirteen postgraduate courses both on regular and part time basis. An excellent platform is provided to the researchers leading to Ph.D degree Guru Nanak Dev Engineering College (An Autonomous College under UGC Act), established in 1956, has been striving persistently in the field of education by comprehending the need for upliftment program.

# ABOUT THE DEPARTMENT

who have spread their wings all over the globe. The alumni of the department are not only excelling in India but also in the Silicon Valley and other hubs of electronics technology. They are at the forefront of the telecom revolution of the last 20 years and manning pivotal positions in telecom operators in India, South East Asia, Pacific region and Africa. In the last three years, as a part of an academically autonomous system, the department has been taken numerous quality initiative including revamping of syllabi so that it is in tune with the outcome based system as proposed in the Washington Accord. The Department of Electronics and Communication Engineering, established in 1981, is one of the most dynamic departments of Guru Nanak Dev Engineering College. The department is currently running UG and PG courses in Electronics and Communication Engineering and has around 15 scholars enrolled for doctorate in areas like Antenna Design, VLSI, Optical communication etc. Ever Design, VLSI, Optical communication etc. Ever since its inception, the department has been the hub of academic excellence through some great teachers who have spread their wings all over the globe. The

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### About the GNDEC, Ludhiana.....

Guru Nanak Dev Engineering College, one of the prestigious, oldest and minority institution of Northern India, was established under the aegis of Nankana Sahib Education Trust (NSET) in 1956. NSET was founded in the memory of the most sacred temple of Nankana Sahib, birth place of Guru Nanak Dev Ji. The main mission for establishing Guru Nanak Dev Engineering College was 'Removal of Economic Backwardness through Technology'. To fulfill this commitment, a unique admission criterion was adopted by NSET by reserving 70% seats for the students of rural areas.

The College is affiliated with I.K.Gujral Punjab Technical University (IKGPTU), Kapurthala. The college courses are approved by AICTE, New Delhi. This is the first Engineering College of Punjab, which was conferred Autonomous Status by University Grants Commission (UGC), New Delhi in 2012 under section 2(f) and 12(B) of UGC Act 1956. The institute runs seven Engineering Branches (Civil, Mechanical, Electrical, Electronics & Communication, Computer Science, Information Technology and Production) at UG level and 15 PG level courses including MBA, MCA. The college has Ph.D in all engineering branches and it is a QIP Centre under AICTE for Ph.D in streams viz. Civil Engineering, Mechanical Engineering and Electrical Engineering.

### **About the NITTTR.....**

The institute started with long term teachers' training programmes for polytechnic teachers in 1967 and was also entrusted with the responsibility of promoting curriculum development work for the states in the region. To improve the competence of teachers for implementing new curricula designed by this institute, short term courses have been offered since 1967. The institute established a Media Centre in 1981 for preparing print & non-print instructional materials.

The institute also set up the department of Rural Development and the department of Entrepreneurship Development to assist polytechnics in directing their efforts towards training manpower and disseminat-

institute has been guiding and assisting the states in the areas of Educational Planning and Management. Since 1992, the institute started offering Regular Master of Engineering Programmes in (i) Engineering Education and (ii) Manufacturing Technology. In the year 1994, two more courses namely Master of Engineering in Construction Technology and Management and Computer Science and Engineering were added. In the year 1998, two more Master of Engineering Programmes in Instrumentation and Control and Electronics and Communication Engineering were added, and, since 2005, the institute started offering Modular Master of Engineering Programmes in all the above disciplines. All these programmes, being offered for teachers of technical institutions and their administrators, professionals from industry and general candidates, are duly approved by AICTE and affiliated to Panjab University, Chandigarh. Since June 2001, this institute has been conducting short term courses in various subjects for faculty of Engineering Colleges in addition to organizing AICTE sponsored Induction Training Programmes.

### **Objectives of Course.....**

The programme aims at enabling the participating teachers to understand the environmental issues and management of such issues in case of Buildings Design.

### Course Content.....

Global Environmental Issues
Concept and importance of Green Buildings
Components of Green Buildings
Design & Planning for Green Buildings
Energy Conservation Building Code.
Rating Systems for Green Building
Case Studies

### Course Coordinator .....

Er. Himmi Gupta Assistant Professor Civil Engineering Department e-mail: himmigupta@nitttrchd.ac.in 0172-2759743

### REGISTRATION.....

Kindly register for the course using following link **Register here** 

NITTTR, Chandigarh and Guru Nanak Dev Engineering College, Ludhiana

**Jointly Organizes** 

### **Green Building** and Techniques

17th—21st December, 2018





at

Guru Nanak Dev
Engineering College,
Gill Road, Gill Park,
Ludhiana (Punjab)-141006
Website: www.ce.gndec.ac.in

Scanned copy of duly Completed Registration Form along with 1-page write-up (reasons to attend this course) and DD/Cheque should be sent by Email to <a href="mailto:pkbhambri@gndec.ac.in">pkbhambri@gndec.ac.in</a> or posted to

Dr. Pankaj Bhambri, Course Coordinator,

Department of Information Technology, Guru Nanak Dev Engineering College, Ludhiana, Punjab, 141006

### Please send a **single email** with attachments as:

(a) 1-page write-up (b) Scanned copy of the filled-in and signed form. (c) Scanned copy of DD/Cheque. The title of the email should be "QIP STC on AI, Big Data and HPC Aligned Applications-A Modern Approach of Computing".

Note: Bring original Registration Form and DD/Cheque at the Time of Registration.

### **IMPORTANT DATES**

Last Date for Receipt of Application: 24<sup>th</sup> Dec., 2018 Intimation of Acceptance: 27<sup>th</sup> Dec., 2018 Short Term Course Dates: Dec. 31<sup>st</sup> 2018 – Jan. 04<sup>th</sup> 2019

Incomplete application forms will not be entertained.

### **VENUE FOR COURSE**

Consultancy Hall, GNDEC, Ludhiana.

### **TIME & DATE FOR REGISTRATION**

09:00 a.m. of Dec. 31st, 2018 at the venue of the course.

### **ELIGIBILITY**

Faculty/ Research scholars (Engineering/ Science/ Management discipline) of degree level engineering colleges recognized by AICTE/UGC/MHRD are eligible to attend the course. Related Research scholars/personnel from R&D/ industrial organizations will also be considered subject to availability of seats.

### **COURSE EVALUATION**

Participants would be awarded 'Course Completion Certificate' on attending the course as per QIP norms.

### REGISTRATION

There is no registration fee for the course. All short-listed candidates are required to confirm their participation by sending a Cheque/Demand Draft of Rs. 1000/- (One thousand) in the name of "PRINCIPAL, GNDEC, Ludhiana" as a caution fee. This amount will be refunded to the participant if he/she attends the course. In case a participant does not attend the course, the above amount will be forfeited. Short listing and confirmation of eligible candidates will be done on a first come first basis (on receipt of registration form through email).

### TRANSPORT, BOARDING & LODGING

Participants are entitled for to and fro 2<sup>nd</sup> class (Sleeper Class or AC) railway/bus fare by the shortest route from their organization to GNDEC, Ludhiana as per QIP norms. Accommodation will be provided in the students Hostels or Guest House on sharing basis. DA will not be permissible.

### **CHIEF PATRON**

Dr. Sehijpal Singh, Principal

### **FACULTY COORDINATOR**

Dr. Kiran Jyoti, Assistant Prof. & HOD (IT) it@gndec.ac.in; +91 98722-24422

### **COURSE COORDINATOR**

Dr. Pankaj Bhambri, Assistant Prof. (IT) pkbhambri@gndec.ac.in; +91 98148-28414

### **ORGANIZING COMMITTEE**

Faculty and Staff, Department of Information Technology

### **RESOURCE PERSONS**

- Dr. S.S. Anand (Sabudh Foundation, New Delhi)
- Dr. S.S. Jha (IIT, Ropar)
- Dr. Geeta Sikka (NIT, Jalandhar)
- Dr. Kuldeep Kumar (NIT, Jalandhar)
- Dr. Nirmal Kaur (UIET, Chandigarh)
- Er. Vipin Gupta (U.Net Solutions, Moga)
- Er. Amit Deoger (NITTTR, Chandigarh)
- Er. Navjot Singh (AlertEnterprise, Chandigarh)
- and many more...

### QIP Short Term Course

on

### Al, Big Data and HPC Aligned Applications - A Modern Approach of Computing

Dec. 31st 2018 - Jan. 04th 2019



### Organized by

Department of Information Technology Guru Nanak Dev Engineering College Ludhiana, Punjab – 141006

### **ABOUT THE INSTITUTE & DEPARTMENT**

Guru Nanak Dev Engineering College, Ludhiana (An Autonomous College under UGC Act), established in 1956, oldest and premier institute of Punjab, is providing education in the field of engineering by comprehending the need for upliftment of rural youth in highly competitive, technologicallyelevated society, in particular, and understanding the thirst for knowledge of students and quenching it in general. The College has been declared an Autonomous College by UGC, New Delhi in 2012. This college has the privilege of starting Ph.D. degree under Quality Improvement Programme (QIP) by AICTE, New Delhi. Apart from offering various Accredited graduate level B.Tech. courses vis-a-vis Civil Engineering, Computer Science & Engineering, Electrical Engineering, Electronics & Communication Engineering, Mechanical Engineering, Production Engineering, Information Technology; the institute imparts instruction in eleven Postgraduate M.Tech. courses both on regular and part time basis. An excellent platform is provided to the researchers leading to Ph.D. degree program.

Department of Information Technology was established in 2001. Since its inception, the department has contributed in a big way to the society by providing technical manpower for various fields in India and abroad. Around 1000 graduates and 100 post graduates from the department are settled in very good positions all over the Globe. At present, the department is proud to get best merit students in the region. The faculty of the department continues to provide new frontiers of technical knowledge to the students by imparting quality education and research. The UG course is NBA accredited and is keeping pace with the latest developments in engineering education.

### **ABOUT THE COURSE**

This Short Term Course will provide opportunity to understand and apply knowledge to complex computational challenges of modern time like scaling computational resources and rapid data growth. Developing key insight about the digital disruption and shift in the computation paradigm will also be the focus area. Broad subject areas for this course are Machine Learning, Natural Language Processing, Data Analytics, Parallel and Distributed Computing. This course will create a platform for industry and academia luminaries to share best practices and techniques to help realize the potential of these technologies.

### Course Objectives:

- To expose the faculty/ research scholars/ industry based persons in emerging technologies in the area of AI, Bigdata and HPC aligned applications.
- To provide the practical foundation level training that enables insight into digital disruption and accelerating machine learning on Al through performance libraries and platforms.

### **Course Contents:**

- 1. AI, Bigdata and HPC Aligned Applications
- 2. Exposure to Analysis and Interpretation of Data
- 3. Al, Bigdata and HPC Research Methods
- 4. Exploration of Contemporary Research on Teaching and Learning
- 5. Real Life Implementation Scenarios
- 6. Case studies
- 7. Lab sessions

### **Course Timings:**

Session wise Schedule (Dec. 31st 2018 – Jan. 04th 2019)

Session I 09:00 a.m. to 10:30 a.m.

Tea Break (10:30 a.m. to 10:45 a.m.)

Session II 10:45 a.m. to 12:15 p.m.

Lunch (12:15 p.m. to 01:00 p.m.)

Session III 01:00 p.m. to 02:30 p.m.

Tea Break (02:30 p.m. to 02:45 p.m.)

Session IV 02:45 p.m. to 04:15 p.m.

### **COURSE BENEFICIARIES**

Faculty/ Research Scholars involved in Teaching/ Research/ Consultancy work

### **BROAD SUBJECT AREA**

Information Technology, Computer Science & Engineering

### RELATED DISCIPLINES

Computer Applications, CS, ECE & EE etc.

**NOTE:** For additional copies of the registration form, use a photocopy of this brochure or type in the format given. For further details: <a href="https://it.gndec.ac.in/">https://it.gndec.ac.in/</a>

### QIP Short Term Course

on

### AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing Dec. 31st 2018 - Jan. 04th 2019

### Registration Form

Name (in block letters):
Designation:
Mailing Address:
Age: Sex (M/F):
D.D./Cheque No Date
Bank Name:
Mobile:
Email:
Educational Qualifications:
Prior Exposure to such program: Yes / No
Accommodation Required in Campus: Yes/ No
Signature of Applicant:

Sponsorship & Signature of Head of the College / Institute (with date & seal).

(Important: By signing above head of the College/Institute certifies that applicant is a faculty member of degree level engineering college recognized by AICTE)

Form should be complete otherwise the application will be rejected

### ABOUT THE COURSE

Machine learning is an essential approach to analyze and extract useful information from a wide range of big data; it provides the fundamental tools for big data analytics. By developing fast and efficient algorithms and data-driven models for realtime processing of data, machine learning is able to produce accurate results and analysis. In the past decade, machine learning has been applied in Google's self-driving car, image recognition, computer vision, speech recognition and natural language processing, intelligent robotics, smart cars, automation, online search and recommendations, financial trading and risk management, healthcare, and personal and public security. Machine learning is so pervasive today that we probably use it dozens of times a day without knowing it.

This course aims to introduce the participants to Machine Learning and its various applications. In particular, the course will start with the introduction of machine learning followed by algorithms used in machine learning. Next, some applications shall be covered and state of the art technologies with hands on exercises will be provided to give insights.

### COURSE CONTENTS

- Introduction to Artificial Intelligence and Machine Learning
- Significant parameters and issues in Machine Learning
- 3. Machine Learning Algorithms
- Applications of Machine Learning in various disciplines
- 5. Hands on sessions on relevant tools

### **IMPORTANT DATES**

Submission of Registration form by 25.01.2019
Acceptance Notification by 30.01.2019

### CONTACT DETAILS

### Dr. Sandeep Singh Gill

Professor & Head

Department of Electronics & Communication Engg.
Guru Nanak Dev Engineering College
Gill Road, Gill Park, Ludhiana,

Punjab - 141006

E-mail:gndec.ece.qip@gmail.com

Phone: 0161-5064576(O)

### PATRON

Dr. Sehijpal Singh (Principal)
Guru Nanak Dev Engineering College, Ldh.

### COORDINATOR (s)

Dr. Sandeep Singh Gill Prof. Tarandeep Singh

### **ORGANISING COMMITTEE**

Prof. Ameeta Seehra Prof. Chahat Jain

Dr. Narwant Singh Prof. Kuldeepak Singh

Dr. Balwinder Singh Prof. Harminder Kaur Aulakh

Dr. Munish Rattan Prof. Daljit Singh

Dr. Baljeet Kaur Prof. Naazia Makkar

Prof. Harminder Kaur Prof. Preeti Pannu

Prof. Navneet Kaur Prof. Shivmanmeet Singh

Prof. Gurpurneet Kaur

QIP SHORT TERM COURSE ON

### Machine Learning Applications in Engineering

(4th-9th February, 2019)



### COORDINATOR (s)

Dr. Sandeep Singh Gill Prof. Tarandeep Singh

**ORGANISED BY** 

### Department of

ELECTRONICS & COMMUNICATION ENGINEERING

### **Guru Nanak Dev Engineering College**

Gill Road, Ludhiana, Punjab-141006

Phone: 0161-5064576 (O) E-mail: gndec.ece.qip@gmail.com

### ABOUT THE INSTITUTE

Guru Nanak Dev Engineering College (An Autonomous College under UGC Act), established in 1956, has been striving persistently in the field of education by comprehending the need for upliftment of rural youth in highly competitive, technologically-elevated society, in particular, and understanding the thirst for knowledge of students and quenching it in general. The College has been declared an Autonomous College by UGC, New Delhi on 17.8.2012. This college has the privilege of starting Ph.D degree under Quality Improvement Programme (QIP) by AICTE, New Delhi. Apart from offering various B.Tech courses, the institute also imparts thirteen Postgraduate courses both on regular and part time basis. An excellent platform is provided to the researchers leading to Ph.D degree program.

### ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering, established in 1981, is one of the most dynamic departments of Guru Nanak Dev Engineering College. It was the first diversification initiative by the college, 25 years after its establishment. The department is currently running UG and PG courses in Electronics and Communication Engineering and has around 15 scholars enrolled for doctorate in areas like Antenna Design, VLSI, Optical communication etc. Ever since its inception, the department has been the hub of academic excellence through some great teachers who have spread their wings all over the globe. The alumni of the department are not only excelling in India but also in the Silicon Valley and other hubs of Electronics Technology. They are at the forefront of the Telecom revolution of the last 20 years and manning pivotal positions in Telecom operators in India, South East Asia, Pacific region and Africa. In the last two years, as a part of an academically autonomous system, the department

has taken numerous quality initiatives including revamping of syllabi so that it is in tune with the outcome based system as proposed in the Washington Accord.

ECE Department is continuously maintaining strong International collaboration with outside world in form of joint research initiatives, editorial board memberships of reputed International journals, serving on academic bodies in various universities and industry institutes, joint research initiatives.

### REGISTRATION

There is no registration fee for participants from academic institutes. All short-listed candidates are required to confirm their participation by sending the scanned copy of duly filled Registration form by e-mail to gndec.ece.qip@gmail.com.

Shortlisting and confirmation of eligible candidates will be on a first come first served basis. Incomplete application forms will not be entertained. Registration forms should reach the coordinator by 25.01.2019.

### ELIGIBILITY

Faculty members and research scholars of degree level engineering colleges recognized by AICTE are eligible to attend the course. Course being interdisciplinary in nature, faculty and research scholars of all engineering branches are eligible to apply.

Course Completion Certificate will be given only if the attendance criterion is satisfied.

### TRANSPORT BOARDING & LODGING

Participants are entitled for II class (Sleeper Class) or AC railway fare to and fro by the shortest route from college to GNDEC, Ludhiana. Accommodation will be provided in the students Hostels or Guest House on sharing basis.

### QIP SHORT TERM COURSE ON

### Machine Learning Applications in Engineering

(4th-9th February, 2019)

### REGISTRATION FORM

Name (in block letters):
Gender:
Qualification:
Designation/Department:
Organization:
Address:
Mobile:
Email:
Accommodation Required in Campus: YES / NO
Signature of Participant

Sponsorship & Signature of Head of the College / Institute (with date & seal).

(Important: By signing above, head of the College/Institute certifies that applicant is a faculty member of degree level engineering college recognized by AICTE)

### About the GNDEC, Ludhiana.....

Guru Nanak Dev Engineering College (GNDEC) was established by the Nankana Sahib Educational Trust (NSET) in the year 1956. NSET was founded in the memory of the most sacred temple of Nankana Sahib, birth place of Guru Nanak Dev Ji. The main mission for establishing Guru Nanak Dev Engineering College was 'Removal of Economic Backwardness through Technology'. To fulfill this commitment, a unique admission criterion was adopted by NSET by reserving 70% seats for the students of rural areas. At present seven undergraduate and thirteen post graduate courses are being offered in Engineering apart from MBA and MCA. The college is an QIP centre for Ph.D. admissions by AICTE in Civil Engineering, Mechanical Engineering and Electrical Engineering.

### **About the Department.....**

Civil Engineering Department has been accredited since 2004 by NBA and also by NAAC. It is running three full time PG programmes (Structural Engineering.; Geotechnical Engineering.; Environmental Science and Engineering) and one part-time PG programme (Soil Mechanics and Foundation Engineering) along with UG programme in Civil Engineering in two shifts. The department has the state of art facilities for R&D activities and is actively engaged in providing consultancy services to different government and private sector in and around Punjab with an annual turnover of approximately Rs one crore. The department has been declared as QIP Centre for Ph.D by AICTE, New Delhi from the session 2011-2012 and is supported under the **FIST** scheme and **TEQIP-II** scheme *GOI*.

### **About the Course.....**

Design is an ever evolving process and it is entirely different now-a-days than it was few decades back due to the better understanding of the material behavior and development of new materials (models). Nevertheless, basic aim of the structural design process remains the same, i.e. to proportion and select various dimensions of the constituent members of the structural system and their other parameters, such as materials, shape, construction technique, etc. so that it must be able to fulfil its purpose with an adequate safety, serviceability and economy; the safety not only implies resistance to the external actions, but it also concentrates on the ductility and robustness of the system.

The material characteristics and its response to the loading play an important role in a design process. It is, therefore, essential that the designer must possess adequate knowledge about the material characteristics going to be used in the construction and their failure envelop describing the safe design stresses.

The course aims to refresh the fundamental concepts related to new and conventional structural materials, their material models and the design of buildings using reinforced concrete as structural material and appraise the participants about the latest developments in the design and practice of RC buildings through some live project/ hands-on-training/ assignments.

### **Course contents.....**

- New material developments
- Material models and the design process
- Fundamental concepts related to the design
- Design of RC members for shear, bending and torsion
- Practical considerations and codal guidelines

QIP sponsored
Short Term Training Program

On

Analysis and Design of RC Buildings:
Concepts and Practice

February 19 - 23, 2019



Organized by

Civil Engineering Department, Guru Nanak Dev Engineering College, Ludhiana (Punjab)- 141006

### How to Reach GNDEC, Ludhiana

The institute is located on Ludhiana-Malerkotla road (also, called Gill road) and is around 6 km from the Bus Stand and 7 km from the Ludhiana railway station. It is well connected to other parts of the country by both rail and road. Important trains, like Shatabdi Express, Rajdhani Express, Howrah Mail, Malwa Express, Golden Temple Mail, Pachim Express and Sachkhand Express halt at the Ludhiana railway station. Regular buses (both, AC and Non-AC) are available from New Delhi and Chandigarh. Nearest airport is at Chandigarh, which is around 100 km from the institute.

### **Resource Persons**

The course will be taught by faculties/experts from the local and near-by academic institutions. Experts from the field will also share their experiences during this course.

### **Eligibility**

The young faculty of Engineering Colleges / University departments and PG Research scholars with civil engineering background can attend the course. The number of participants will be restricted to 30 as per the QIP norms. The certificate of participation will be awarded to the candidates who fulfils the attendance requirements and successfully completes the assignments/ the project work and secures 70% marks in the evaluation.

### Accommodation

A limited number of rooms with shared accommodation are available in the guest house at Guru Nanak Dev Engineering College campus on request on 'first-come first-serve basis'.

### Registration

There is no registration fee for the participants and TA/DA will be paid to the out station participants as per the norms. Since the seats are limited, the registration will be done on *first-come first-serve basis*. The registration process will be considered complete only when the registration form is received from the participant through a proper channel by *15<sup>th</sup> February, 2019*. However, candidates may send an advance copy through email, post or by hand.

### **Organizing Committee**

### Patron

Dr. Sehajpal Singh, Principal
Guru Nanak Dev Engineering College, Ludhiana

### **Advisors**

Dr K.S. Gill, Professor and Head, Dr H.S. Rai, Professor and Dean (Testing & Consultancy), Dr Harpal Singh, Professor, Department of Civil Engineering, Guru Nanak Dev Engineering College, Ludhiana

### **Coordinators**

Dr Gurdeepak Singh and Dr Inderpreet Kaur Department of Civil Engineering, Guru Nanak Dev Engineering College, Ludhiana

### **Faculty Coordinator**

Dr Harvinder Singh Professor, Department of Civil Engineering, Guru Nanak Dev Engineering College, Ludhiana

### Correspondence

Please mail the signed copy of the registration form at the email id: **hs.gndec @gmail.com**Contact no.— 9855965901, 9878190300 (Coordinators)
The hard copy can be addressed to the *Coordinator (STTP -16)*, *Civil Engineering Department, Guru Nanak Dev Engineering College, Gill Road, Ludhiana-141006*.

### Guru Nanak Dev Engineering College, Ludhiana

QIP sponsored Short Term Training Program On

### Analysis and Design of RC Buildings: Concepts and Practice

(February 19 - 23, 2019)

### **REGISTRATION FORM**

Mama	
Name:	
Designation:	
Department:	
Organization	:
	METAL
Tel./Mobile:	
E-mail:	
`	ualification with Specialization:
	tion : Required/Not required
Signature an	d Date:
	Sponsorship Certificate
The applican	t is hereby sponsored and will be allowed
to attend this	course, if selected.

Signature and Seal

Date:....

### RESOURCE PERSONS

Experts from IIT, IIM, Industries and Invited Experts from other Reputed Academic /Research Institutions.

Dr. Kamal K. Jain, IIM, Indore

Dr. Sanjeev K. Sharma, PU Dr. Sanjay Wadwalkar, PU

Dr. Jagwinder Singh, NIT Jalandhar

Dr. Anupama Sharma, PU

Dr. Bhupinder P. S. Chahal, CU

Dr. Jitendra Mohan, Professor EMERITUS, PU.

Dr. Dinesh Gupta, UBS, PU

Dr. Rakesh K. Watts. NITTTR

Dr. Inderdeep Singh, IIT, Roorkee

Dr. Monika Aggarwal, PU

Mr. S.K. Sharma, Joint Director, Nahar Group of Companies & many more ......

### **ELIGIBILITY**

Faculty members from various AICTE approved Engineering & Management Colleges/Institutions can apply. Academicians from various research organizations and industries are eligible to participate in the programme.

### **REGISTRATION**

No registration fee for the course. Short listing and confirmation of eligible candidates will be done on a first come first basis (receipt of registration form through email mbafdp@gndec.ac.in). For registration open the URL link https://gndec.ac.in/sites/default/files/Registration%20formmbafdp.pdf and download the form and scanned the copy of duly completed Registration Form along with 1-page write up (reasons to attend this course should be sent by Email to mbafdp@qndec.ac.in.

### Last date of Registration is 21st May, 2019.

This programme is sponsored by TEQIP-III,MHRD- New Delhi, Guru Nanak Dev Engineering College, Ludhiana.

### **Pedagogical Tools & Techniques -Effective Teaching & Comprehensive Progress.**

(27<sup>th</sup> May -1<sup>st</sup> June, 2019)

### **REGISTRATION FORM**

Name (in block lette	rs):	
		Educational Qualifications:
		Mobile :
Email:		Prior Exposure to such program: Yes / No Accommodation Required in Campus: Yes/ No

### Signature of Head of the College / Institute (with date & seal).

Signature of Applicant

(Important: By signing above head of the College/Institute certifies that applicant is a faculty member of degree level Engineering/Management college recognized by AICTE, New Delhi)

Form should be complete otherwise the application will be rejected.

### **Faculty Development Programme**

### **Pedagogical Tools & Techniques -Effective Teaching & Comprehensive Progress**

(27th May -1st June, 2019)



**ORGANISED BY** 

**GURU NANAK DEV ENGINEERING COLLEGE LUDHIANA** 



under TWINNING WITH:

ATAL BIHARI VAJPAYEE GOVERNMENT **INSTITUTE OF ENGINEERING &** TECHNOLOGY, PRAGATINAGAR, SHIMLA.

### **Sponsored by:**

TEQIP-III, MINISTRY OF HUMAN RESOURCE DEVELOPMENT (NEW DELHI)

> **COURSE COORDINATOR** Dr. Navdeep Kaur

**COURSE CO-COORDINATOR** Dr. Mithilesh Kumar Jha

### PREFACE

Effective teaching has been understood as teaching that is oriented to and focused on students and their learning. Generally, teacher effectiveness is referred to in terms of a focus on student outcomes and the teacher behaviours and classroom processes that promote better student outcomes. The VITAE research suggests that school leadership, professional development and support from colleagues can be important in sustaining teachers' professional identities, their job satisfaction, commitment to teaching, resilience and perceived effectiveness. The active, intentional, and ongoing engagement with diversity in people, in the curriculum, in the co curriculam and in communities (intellectual, social, cultural, geographical) with which individuals might connect - in ways that increase one's awareness content knowledge, cognitive sophistication, and emphatic understanding of the complex ways individuals interact within systems and institutions. Inclusive excellence re-envisions both quality and diversity. Today, increasing attention is being given to the quality of teaching and learning at Institutes across the world. Inclusive Pedagogy advocates teaching practices that embrace the whole student in the learning process who enter the classroom as personal, political, and intellectual beings. The objective of this faculty development programme is to provide faculty an opportunity to embrace effective teaching in a diverse classroom and develop a group of Inclusive Pedagogy Practitioners across the Institutes of learning.

### TOPICS TO BE COVERED

- Concept Dimensions of Teaching & Standard for Effective teaching.
- Role of Knowledge Management in Education.
- Learning Strategies/Techniques for Modern Pedagogy (Video Discussion, Business Games, Case Study, Role Plays and simulations, Experiential Learning, Panel Discussion, Structured Exercises etc.)
- Innovative Teaching Pedagogy: New Educational Paradigm.
- · Work Life Balance/ Harmonizing Interpersonal Relations.
- Enhancing Personal Effectiveness: Simulations & Exercises.
- Kaizen Techniques for Teachers & Avoiding of Halo Effect Error.
- · Student Psychology Problems and Promises.
- · Communication in Classroom/ Engaging Students.
- · Students Motivation Techniques/Leadership.
- · Role of Spirituality in education for Sustainable Development.
- · Role of ICT in Sustainable Development.
- E-Learning & higher Education-Technology & pedagogy.
- Developing Research Acumen: Schemes of TEQIP, UGC & AICTE.
- Portal for online learning: Providing Awareness & Encourage for Enrolment.
- Using Students' Feedback & Self Evaluation.
- · Learning Outcomes Setting of question papers.
- · Design of Rubrics.
- Meeting of Minds: Developing Synergy between Different Stakeholders: Staff, Students, Alumni and Administration -Panel Discussion.

### ABOUT GURU NANAK DEV ENGINEERING COLLEGE, LUDHIANA

Guru Nanak Dev Engineering College, Ludhiana (An Autonomous College under UGC Act), established in 1956, oldest and premier institute of Punjab, is providing education in the field of engineering by comprehending the need for up-liftment of rural youth in highly competitive, technologically-elevated society, in particular, and understanding the thirst for knowledge of students and quenching it in general. The College has been declared an Autonomous College by UGC, New Delhi in 2012. This college has the privilege of starting Ph.D degree under Quality Improvement Programme (QIP) by AICTE, New

Delhi. Apart from offering various Accredited graduate level B.Tech courses vis-a-vis Civil Engineering, Computer Science & Engineering, Electrical Engineering, Electronics & Communication Engineering, Mechanical Engineering, Production Engineering, Information Technology, the institute imparts instruction in eleven Postgraduate M.Tech courses both on regular and part time basis. College also runs Post graduate programs i. e. Master of Business Administration and Master of Computer Application full time regular basis. An excellent platform is provided to the researchers leading to PhD degree program.

### ABOUT ATAL BIHARI VAJPAYEE GOVERNMENT INSTITUTE, SHIMLA

Atal Bihari Vajpayee Government Institute of Engineering & Technology was established in 10 April 2011. It is the Second Government Regional Engineering institute of Himachal Pradesh which is located at Pragatinagar Village Gumma, Teh. Kotkhai, Distt. Shimla. Presently three disciplines has been running under the same roof i.e B.Tech., Polytechnic and I.T.I. B.Tech presently running two courses i.e Computer Science & Engineering and Electronics & Communication Engineering. Polytechnic presently offers three courses i.e Computer Engineering, Electronics & Communication Engg. & Electrical Engg. Three programmes namely Electrical Engineering, Electronics & Communication Engineering and Computer Engineering The B.Tech (Degree programme) has been started with two disciplines namely Electronics & Communication Engineering and Computer Science & Engineering as constituent college of HP Technical University, Hamirpur. For more details visit http://www.abvgiet.org/. This Institution is located at Pragatinagar on the bank of river Giri Ganga about 61 km from Shimla on Shimla Rohru National Highway.

### ABOUT TEQIP -III

Technical Education Quality Improvement Programme was launched in December, 2012 by the Ministry of Human Resource Development; India with the World Bank assistance. The programme was conceived and designed as a long-term project to be implemented in 10-12 years in 3 phases to support excellence and transformation in technical education in the country. The third phase of the programme TEQIP-III was started in 2019. The main objective of TEQIP-III is to improve the quality of engineering education in the existing government institutions in the educationally backward states and also arrange for twining them other institutes. Guru Nanak Dev Engineering College, Ludhiana is twining in arrangement with Atal Bihari Vajpayee Government Institute of Engineering & Technology, Pragatinagar, Shimla.

### CHIEF PATRON

Dr. Sehijpal Singh Principal GNDEC, Ludhiana

### PATRON

COORDINATOR

Dr. Sukhdev Singh
Prof. & Head
t of Rusiness Administra

Deptt. of Business Administration

Dr. Navdeep Kaur Deptt. of
Department of Business Administration
Guru Nanak Dev Engineering College
Gill Road, Ludhiana -141006
Contact No: 9876343366 E-mail: nav271@gmail.com

Abv Govt. insitute of Engineering & Technology Pragatinagar, Shimla- 171202

Pragatinagar, Shimla-171202 Contact No: 8283937139 Email: jhamkin@yahoo.co.in

CO-COORDINATOR

Dr. Mithilesh Kumar Jha

Director/Principal

ORGANIZING COMMITTEE

Dr. Parampal Singh | Ms. Amanjot K Gill | Dr. H. S. Dhall | Ms. Amarpreet Kaur & Staff, Deptt. of Business Administration, Guru Nanak Dev Engineering College, Ludhiana

E-mail for Contact : mbafdp@gndec.ac.in

### COURSE OBJECTIVE

Wireless and optical communication has emerged as a principal theme in today's telecommunication industry. Innovations in optical fiber technology are revolutionizing the world communication by allowing direct transmission of high-speed signals over long distances. Optical fiber communication (OFC) has found numerous applications in Telecommunications, Local area networks, High precision material processing, Sensors, Medicine etc. It is one of the fastest growing technological markets because of relatively compact size of fiber, cost-effective, high efficiency and their stable operation. Wireless Communication is by measure, the fastest expanding segment of the communication industry. Broadband wireless access is supporting rates in excess of 100 Mbps. Subsequently this led to the development of 3G through Code Division for Multiple Access (CDMA) Orthogonal Frequency Division Multiplexing (OFDM) and Multiple Input Multiple Output (MIMO). Representatives of industries and academia have started to look into the technological developments toward the next generation (5G), as the rollout of 4G mobile communication networks takes place. This course will address the latest trends and future challenges that must be met to develop high performance systems.

### COURSE CONTENTS

- \* Fundamentals of Wireless Communication
- \* Recent Advances in Wireless Communication
- \* Introduction to Cognitive Radio and Spectrum Sensing
- \* Software Defined Radio
- \* Intelligent Networks
- \* Fundamentals of Optical Communication
- \* WDM/DWDM Techniques & Components
- \* Non Linear Effects in Optical Fibers
- \* Optical Networks-Overview and Recent Trends
- \* Future in Home Optical Network using POF
- \* Software Tools for Research
- \* Visit to Videocon Telecom Limited Ludhiana

### IMPORTANT DATES

Submission of Registration form by 31.05.2019 Acceptance Notification by 04.06.2019 DATE & VENUE FOR REGISTRATION

10.06.2019 (9AM) at TCC Seminar Hall, GNDEC, Ludhiana

### CONTACT DETAILS

Dr. Baljeet Kaur

Assistant Professor Department of Electronics & Communication Engineering

Guru Nanak Dev Engineering College Gill Park, Ludhiana. Punjab – 141006

> E-mail:gndec.ece.fdp@gmail.com Phone: 0161-5064768 (O)

### CHIEF PATRON

Dr. Sehijpal Singh (Principal) Guru Nanak Dev Engineering College, Ludhiana

### PATRON

Prof. Ameeta Seehra Head,

Department of Electronics & Communication Engineering

### ORGANISING COMMITTEE

Prof. Sandeep Thakur
(ABVGIET, Pragatinagar)
Prof. Anjali Bharti
(ABVGIET, Pragatinagar)
Dr. Narwant Singh
Dr. Balwinder Singh
Dr. Munish Rattan
Prof. Navneet Kaur
Prof. Gurpurneet Kaur

Prof. Gurpurneet Kaur Prof. Chahat Jain Prof. Kuldeepak Singh

Prof. Harminder Kaur Aulakh

Prof. Daljit Singh

Prof. Simranjit Kaur Prof. Tarandeep Singh

Prof. Preeti Pannu

Prof. Shivmanmeet Singh

Prof. Harsimranjit Singh Prof. Gurinder Singh

Prof. Kirandeep Kaur

Prof. Shyna Kalra

Prof. Harleen Kaur

### ONE WEEK TEQIP- III SPONSORED FACULTY DEVELOPMENT PROGRAMME

ON

### "Enabling Technologies for Wireless and Optical Networks"

(10"-14" June, 2019)



### Coordinator Dr. Baljeet Kaur

Co-Coordinator

Prof. Harminder Kaur

Organized by
Department of Electronics and
Communication Engineering

Guru Nanak Dev Engineering College Gill Park, Ludhiana, Punjab – 141006 E-mail: gndec.ece.fdp@gmail.com Phone: 0161-5064768(O)

Atal Bihari Vajpayee Govt. Institute of Engineering and Technology Pragatinagar, Shimla (H.P) -171202

### ABOUT THE INSTITUTES

Guru Nanak Dev Engineering College (An Autonomous College under UGC Act), established in 1956, has been striving persistently in the field of education by comprehending the need for upliftment of rural youth in highly competitive, technologically-elevated society, in particular, and understanding the thirst for knowledge of students and quenching it in general. The College has been declared an Autonomous College by UGC, New Delhi on 17.8.2012. Apart from offering various accredited graduate level B.Tech courses, the institute also imparts thirteen Postgraduate courses both on regular and part time basis. This college has the privilege of starting Ph.D degree under Quality Improvement Programme (QIP) by AICTE, New Delhi.

Atal Bihari Vajpayee Government Institute of Engineering and Technology, a premier engineering institution was established in the year 2011 under the Society Registration Act by the Government of Himachal Pradesh. This institution is situated at Pragatinagar in beautiful apple growing valley on the bank of river Giri Ganga about 60 Km from Shimla. ABVGIET offers technical education in three disciplines B.Tech., Polytechnic and ITI. Presently, CSE and ECE courses are offered in B.Tech. and CSE, ECE and EE in polytechnic. ITI is running five courses i.e. Electrical, Electronics, Fitter, Draughtsman and Motor Vehicle Mechanic.

### ABOUT THE DEPARTMENTS

The Department of Electronics and Communication Engineering, established in 1981, is one of the most dynamic departments of Guru Nanak Dev Engineering College. It was the first diversification initiative by the college, 25 years after its establishment. The department is currently running UG and PG (NBA Accredited) courses in Electronics and Communication Engineering and has around 15 scholars enrolled for doctorate. Ever since its inception, the department has been the hub of academic excellence through some great teachers who have spread their wings all over the globe. The alumni of the department are not only excelling in India but also in the Silicon Valley and other hubs of Electronics Technology. They are at the forefront of the Telecom revolution since the last 20 years. In the last two years, as a part of an academically autonomous system, the department has been taking numerous quality initiative including revamping of syllabi so that it is in tune with the outcome based system as proposed in the Washington Accord of Accreditation.

The department is exhibiting a very good pass percentage of students usually above 95% for UG/PG Courses . ECE Department is continuously maintaining strong International collaboration with outside world in form of joint research publications, editorial board memberships of reputed International journals.

The department of Electronics and Communication Engineering (ECE) at Atal Bihari Vajpayee Government Institute of Engineering and Technology offers a 4-year B. Tech. programme in Electronics and Communication Engineering. The department has well experienced and dynamic faculty members.

### REGISTRATION

There is no registration fee for participants from academic institutes. All short-listed candidates are required to confirm their participation by sending the scanned copy of duly filled Registration form by Email to gndec.ece.fdp@gmail.com, latest by 31.05.2019

Shortlisting and confirmation of eligible candidates will be on a first come first served basis. Incomplete application forms will not be entertained.

### ELIGIBILITY

Faculty members of degree level engineering colleges recognized by AICTE are eligible to attend the course. Applications from various fields like ECE/EE/CSE/IT/Applied science and other related disciplines are invited.

Course Completion Certificate will be given if the attendance criterion is satisfied.

### ACCOMMODATION

Accommodation will be provided in the students Hostels or Guest House on sharing basis. No TA/DA will be paid.

### ONE WEEK TEQIP- III SPONSORED FACULTY DEVELOPMENT PROGRAMME ON

### "Enabling Technologies for Wireless and Optical Networks"

(10"-14" June, 2019)

### REGISTRATION FORM

N	ame (BLOCK LETTERS):
G	ender:
Q	ualification:
D	esignation/Department:
o	rganization:
A	ddress:
M	obile:
E	mail :
Δ	ccommodation in Campus: YES / NO

Signature of Participant

Sponsorship & Signature of Head of the College / Institute (with date & seal).

(Important: By signing above, Head of the College/Institute certifies that applicant is a faculty member of degree level engineering college recognized by AICTE)

### COURSE OBJECTIVES

Data analytics is the process of examining datasets to draw conclusions about the information they contain, increasingly with the aid of specialized systems and software. Proper analysis reveals key user and customer trends and facilitates the social network's alignment of content, layout and overall strategy. This FDP would provide guidelines to explore the area of data analytics and its application areas. The participants would learn to develop inventive methods for solving problems related to diverse computational fields. They would get exposure to the tools, methods and current research trends prevailing in this area so that it could be used to benefit the society.

### **COURSE CONTENTS**

- · Data exploration and pre-processing
- Identifying the emerging research trends in data analytics
- · Data Science and advanced data models
- Data science analytical research challenges for engineers
- Hadoop, applications of Big data and predictive analytics
- Machine learning at scale
- · Classification and clustering on Big data
- Applications of data science for NLP
- · ANN, GA in problem solving
- Deep learning
- · Data analytics for social good and applications

### RESOURCE PERSONS

The FDP will be conducted by highly qualified and experienced experts from various esteemed institutions including Thapar University Patiala, Panjab University, Chandigarh, Punjabi University Patiala, Tatras Data Delhi and Sabudh Foundation Mohali.

### **CHIEF PATRON**

### Dr. Sehijpal Singh

Principal

Guru Nanak Dev Engg. College, Ludhiana

### CHAIRMAN

### Dr. Parminder Singh

Professor & Head (CSE)
Guru Nanak Dev Engg. College, Ludhiana

### COORDINATOR

### Dr. Sumeet Kaur Sehra

Assistant Professor (CSE)
Guru Nanak Dev Engg. College, Ludhiana

### Co-COORDINATOR

### Er. Ravi Kumar

Assistant Professor (CSE)
ABVGIET, Pragati Nagar, (H.P)

### **IMPORTANT DATES**

LAST DATE FOR RECEIPT OF APPLICATION

17th June, 2019

INTIMATION OF ACCEPTANCE

19th June, 2019

FACULTY DEVELOPMENT PROGRAMME

DATES

24th - 29th June, 2019

### **TEQIP-III Sponsored**

One Week Faculty

Development Programme

On

### DATA ANALYTICS-TRENDS AND APPLICATIONS

(24th-29th June, 2019)



### **ORGANISED BY**

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING
GURU NANAK DEV ENGINEERING COLLEGE
LUDHIANA, (Pb.)- 141006

### AND

DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING
ATAL BIHARI VAJPAYEE GOVERNMENT
INSTITUTE OF ENGINEERING &
TECHNOLOGY, PRAGATINAGAR, SHIMLA (H.P)-171202

For details visit : http://gndec.ac.in/fdp\_da/





### ABOUT GURU NANAK DEV ENGINEERING COLLEGE LUDHIANA (PB.)

Guru Nanak Dev Engineering College, Ludhiana (An Autonomous College under UGC Act), established in 1956, oldest and premier institute of Punjab, is providing education in the field of engineering by comprehending the need for upliftment of rural youth in highly competitive, technologically-elevated society, in particular, and understanding the thirst for knowledge of students and quenching it in general. The College has been declared an Autonomous College by UGC, New Delhi in 2012. This college has the privilege of starting Ph.D. degree under Quality Improvement Programme (QIP) by AICTE, New Delhi. Apart from offering various Accredited graduate level B.Tech courses vis-a-vis Civil Engineering, Computer Science & Engineering, Electrical Engineering, Electronics & Communication Engineering, Mechanical Engineering, Production Engineering, Information Technology, the institute imparts instruction in eleven Postgraduate M.Tech courses both on regular and part time basis. College also runs Post graduate programs i. e. Master of Business Administration and Master of Computer Application full time regular basis. An excellent platform is provided to the researchers leading to PhD degree program.

### ABOUT ATAL BIHARI VAJPAYEE GOVERNMENT INSTITUTE, PRAGATI NAGAR, (H.P)

Atal Bihari Vajpayee Government Institute of Engineering & Technology was established in 10 April 2011. It is the Second Government Regional Engineering institute of Himachal Pradesh which is located at Pragatinagar Village Gumma, Teh. Kotkhai, Distt. Shimla. Presently three disciplines has been running under the same roof i.e B.Tech., Polytechnic and I.T.I. B.Tech presently running two

courses i.e Computer Science & Engineering and Electronics & Communication Engineering. Polytechnic presently offers three courses i.e Computer Engineering, Electronics & Communication Engg. & Electrical Engg. Three programmes namely Electrical Engineering, Electronics & Communication Engineering and Computer Engineering The B.Tech (Degree programme) has been started with two disciplines namely Electronics & Communication Engineering and Computer Science & Engineering as constituent college of HP Technical University, Hamirpur. This Institution is located at Pragatinagar on the bank of river Giri Ganga about 61 km from Shimla on Shimla Rohru National Highway.

### ABOUT TEQIP-III

Technical Education Quality Improvement Programme was launched in December, 2012 by the Ministry of Human Resource Development; India with the World Bank assistance. The programme was conceived and designed as a longterm project to be implemented in 10-12 years in 3 phases to support excellence and transformation in technical education in the country. The third phase of the programme TEQIP-III was started in 2019. The main objective of TEQIP-III is to improve the quality of engineering education in the existing government institutions in the educationally backward states and also arrange for twining them other institutes. Guru Nanak Dev Engineering College, Ludhiana is twining in arrangement with Atal Bihari Vajpayee Government Institute of Engineering & Technology, Pragatinagar, Shimla.

### **ACCOMMODATION**

Accommodation will be provided in the students Hostels or Guest House on sharing basis. No TA/DA will be paid.

### REGISTRATION

There is no registration fee for participants from academic institutes. Interested persons should register at "http://gndec.ac.in/fdp\_da/". All short-listed candidates are required to confirm their participation by sending the scanned copy of duly filled Registration form by email to fdp.da.cse@gmail.com latest by 17th June, 2019. Shortlisting and confirmation of eligible candidates will be on a first come first serve basis. Incomplete application forms will not be entertained. Hard copy of duly filled form should be submitted at Registration desk on 24th June, 2019.

### **ELIGIBILITY**

Faculty members of degree level engineering colleges recognized by AICTE are eligible to attend the course. Applications from various fields like ECE, EE, CSE, IT and Applied science and other related disciplines are invited. Course Completion Certificate will be given if the attendance criterion is satisfied.

### ADDRESS FOR CORRESPONDENCE

Dr. Sumeet Kaur Sehra

Assistant Professor

Department of Computer Science & Engineering

Guru Nanak Dev Engineering College, Ludhiana Punjab, 141006.

Email: fdp.da.cse@gmail.com Contact no. +91 9914055900



### CRITERIA 6.3.3

### List of Participants of Professional Development/Administrative Training Programs

### NATURE INSPIRED COMPUTATIONAL TECHNIQUES FOR ENGINEERING AND SCIENCES (2014)

	Name of	(2011)	Institute
S. No.	Participant <b>Participant</b>	Designation	Name
501100	Ajay Shiv	Assistant	GNDEC,
1	Sharma	Professor	Ludhiana
_	Amanpreet	Assistant	GNDEC,
2	Kaur Sodhi	Professor	Ludhiana
	Balwinder	1 101000	
	Singh	Assistant	GNDEC,
3	Dhaliwal	Professor	Ludhiana
		Assistant	GNDEC,
4	Chahat Jain	Professor	Ludhiana
			LOVELY
			PROFESS
			IONAL
	Cherry	Assistant	UNIVERS
5	Bhargava	Professor	ITY
			LUDHIAN
			A
			GROUP
			OF
			COLLEGE
	Daljit Singh	Assistant	S,CHOW
6	Sekhon	Professor	KIMAN
	Gagandeep	Assistant	GNDEC,
7	Singh Sodhi	Professor	Ludhiana
	Gurdeepak	Assistant	GNDEC,
8	Singh	Professor	Ludhiana
	Gurjot Kaur	Assistant	GNDEC,
9	Walia	Professor	Ludhiana
	Gurpurneet	Assistant	GNDEC,
10	Kaur	Professor	Ludhiana
	Harmeet	Assistant	GNDEC,
11	Singh Gill	Professor	Ludhiana
	Harminder	Assistant	GNDEC,
12	Kaur	Professor	Ludhiana

			~ ~ ~
			CGC,
l	Harpreet	Assistant	GHARUA
13	Kaur	Professor	N
			LUDHIAN
			A
			GROUP
			OF
			COLLEGE
	Harpreet	Assistant	S,CHOW
14	Kaur	Professor	KIMAN
			SHRI
			GURU
			GRANTH
			SAHIB
			WORLD
	Harsimranjit	Assistant	UNIVERS
15	Singh Gill	Professor	ITY
	Iqwinderpreet		GNDEC,
16	Singh	Professor	Ludhiana
		Assistant	GNDEC,
17	Jagjit Kaur	Professor	Ludhiana
			CGC,
	_	Assistant	GHARUA
18	Jasmeet Kaur		N
	Jaswinder	Assistant	GNDEC,
19	Singh	Professor	Ludhiana
	Kanwaljit	associate	GNDEC,
20	Singh	Professor	Ludhiana
	Kanwalpreet	Assistant	GNDEC,
21	Sahni	Professor	Ludhiana
			UCOE,PU
			NJABI
			UNIVERS
		Assistant	ITY,PATI
22	Kiranjot Kaur		ALA
	Kuldeepak	Assistant	GNDEC,
23	Singh	Professor	Ludhiana
	Munish	Assistant	GNDEC,
24	Rattan	Professor	Ludhiana
			CGC,
<b>.</b> .	Nancy	Assistant	GHARUA
25	Ramanpreet	Professor	N

	Narwant	Assistant	GNDEC,
26	Singh Grewal		Ludhiana
	<u> </u>	Assistant	GNDEC,
27	Navneet Kaur		Ludhiana
	Parminder	associate	GNDEC,
28	Singh	Professor	Ludhiana
	<u> </u>		CGC,
	PrabhjotSing	Assistant	GHARUA
29	h	Professor	N
	Randhir	Assistant	GNDEC,
30	Singh	Professor	Ludhiana
	8	Assistant	GNDEC,
31	Rohin Gupta	Professor	Ludhiana
	rteinin e up u	110100001	RIMT,
			MANDI
	Rohit	Assistant	GOBIND
32	Moudgil	Professor	GARH
	1,10 0.00811	Assistant	GNDEC,
33	Ruby Gupta	Professor	Ludhiana
22	Rupinderjit	Assistant	GNDEC,
34	Singh	Professor	Ludhiana
<i>3</i>	Sandeep	110165501	GNDEC,
35	Singh Gill	Professor	Ludhiana
33	Singh Gin	110103501	Ladinana
			LUDHIAN
			A
			GROUP
			OF
			COLLEGE
		Assistant	S,CHOW
36	Savita Rani	Professor	KIMAN
50	Suvita Italii	110165501	CGC,
	Simrandeep	Assistant	GHARUA
37	Singh	Professor	N
51	Simranjit	Assistant	GNDEC,
38	Singh	Professor	Ludhiana
50	Sukhminder	Assistant	GNDEC,
39	Singh	Professor	Ludhiana
5)	Billgii	1 10103501	UIET,PU,
	Vishal	Assistant	CHANDI
40	Sharma	Professor	GARH
-T1/	DHAHHA	1 10109901	

**List of Participants** 

S.No.	Name	Designation	Affiliation
1	Ajay Shiv Sharma	Assistant Professor	GNDEC, LUDHIANA
2	Akshay Girdhar	Associate Professor	GNDEC, LUDHIANA
3	Amandeep Kaur	Assistant Professor	BHSBIET, LEHRAGAGA
4	Amardeep Singh	Application Developer	PAU, LUDHIANA
5	Amit Kamra	Assistant Professor	GNDEC, LUDHIANA
6	Angaddeep Singh	Lecturer	SRSGPC, LUDHIANA
7	Anil Kumar	Librarian	GNDEC, LUDHIANA
8	Anurag Sharma	Research Scholar	GNDEC, LUDHIANA
9	Arunjot Kaur	Assistant Professor	GNDEC, LUDHIANA
10	Bindia Tarika	Computer Programmer	GNDEC, LUDHIANA
11	Blossom	Assistant Professor	GNDEC, LUDHIANA
12	Charanjeev Kaur	Assistant Professor	DBU, MANDI GOBINDGARH
13	Deepika	Assistant Professor	LCET, LUDHIANA
	Deepinder Kaur		
14	Dhaliwal	Assistant Professor	DBU, MANDI GOBINDGARH
15	Dipti Kunra	Assistant Professor	GNDEC, LUDHIANA
16	Gagandeep Kaur	Lecturer	BCET, LUDHIANA
17	Gaurav Tandon	Assistant Professor	DBU, MANDI GOBINDGARH
18	Gurdit Singh	Research Scholar	PECUT, CHANDIGARH
19	Gurpreet Kaur	Research Scholar	GNDEC, LUDHIANA
20	Gurpreet Singh	Assistant Professor	GGI, KHANNA
21	Gursharan Singh	Lecturer	BCET, LUDHIANA
22	Gurvir Singh	Research Scholar	GNDEC, LUDHIANA
23	Hanit Karwal	Assistant Professor	GGI, KHANNA
24	Harbhag Singh	Assistant Professor	GNDEC, LUDHIANA
25	Hardeep Singh Handa	IT Incharge	RELIANCE, LUDHIANA
26	Iardeep Singh Jawand	HOD	GNDPC, LUDHIANA
27	Harmanjeet Kaur	Research Scholar	GNDEC, LUDHIANA
28	Harminder Kaur	Research Scholar	GNDEC, LUDHIANA
29	Harmohan Singh Dhall	Assistant Professor	GNDEC, LUDHIANA
30	Harpreet Kaur Grewal	Associate Professor	GNDEC, LUDHIANA
31	Inderjeet Singh	Computer Programmer	GNDEC, LUDHIANA
32	Jagreet Kaur	Assistant Professor	PU, CHANDIGARH
33	Jappreet Kaur	Assistant Professor	GNDEC, LUDHIANA
34	Jasbir Singh Saini	Associate Professor	GNDEC, LUDHIANA
35	Jaspreet Kaur	Lecturer	LPU DEP CENTRE, LUDHIANA
36	Kamaldeep Arora	Assistant Professor	GGI, KHANNA
37	Kamaldeep Kaur	Assistant Professor	GNDEC, LUDHIANA
38	Kamaljit Kaur	Assistant Professor	GNDEC, LUDHIANA
39	Karamjit Singh	Lecturer	BCET, LUDHIANA
40	Kiran Jyoti	Assistant Professor	GNDEC, LUDHIANA
41	Kulvinder Singh Mann	Associate Professor	GNDEC, LUDHIANA
42	Kulwinder Singh	Assistant Professor	GGI, KHANNA
43	Lolita Singh	Assistant Professor	HJDITE, GUJRAT
44	Lovepreet Kaur Somal	Assistant Professor	GNDEC, LUDHIANA
45	Manmeet Kaur	Lecturer	SRSGPC, LUDHIANA

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46	Manpreet Kaur Grewal	Assistant Professor	GNDEC, LUDHIANA
47	Manpreet Singh	Assistant Professor	GNDEC, LUDHIANA
48	Mohit Kumar	Lecturer	DBU, MANDI GOBINDGARH
49	Pankaj Bhambri	Assistant Professor	GNDEC, LUDHIANA
	Parminder Kaur		
50	Wadhwa	Assistant Professor	GNDEC, LUDHIANA
51	Parminder Singh	Associate Professor	GNDEC, LUDHIANA
52	Prabhjot Kaur	Research Scholar	GNDEC, LUDHIANA
53	Priyanka Arora	Assistant Professor	GNDEC, LUDHIANA
54	Rajan Manro	Assistant Professor	DBU, MANDI GOBINDGARH
55	Randhir Singh	Assistant Professor	GNDEC, LUDHIANA
56	Raninder Kaur	Assistant Professor	GNDEC, LUDHIANA
57	Ranjodh Singh	Assistant Professor	DAV UNI., JALANDHAR
58	Saravpreet Kaur	Assistant Professor	LCET, LUDHIANA
59	Satwinderjit Kaur	Associate Professor	GNDEC, LUDHIANA
60	Shairy	Lecturer	LPU DEP CENTRE, LUDHIANA
61	Sukhjit Singh Sehra	Assistant Professor	GNDEC, LUDHIANA
62	Sumeet Kaur Sehra	Assistant Professor	GNDEC, LUDHIANA
63	Tanpreet Singh	Assistant Professor	AIT, GHARUAN

Two Day workshop on VLSI Design					
	(2015)				
	Name of				
G. N	Participan	Designatio	Institute		
S. No.	t	n	Name		
	Sandeep	Assistant	GNDEC,		
1	Singh Gill	Professor	Ludhiana		
	Ameeta	Assistant	GNDEC,		
2	Seehra	Professor	Ludhiana		
	Narwant	Assistant	GNDEC,		
3	Singh	Professor	Ludhiana		
	Balwinder				
	Singh	Assistant	GNDEC,		
4	Dhaliwal	Professor	Ludhiana		
	Munish	Assistant	GNDEC,		
5	Rattan	Professor	Ludhiana		
	Baljeet	Assistant	GNDEC,		
6	Kaur	Professor	Ludhiana		
	Harminder	Assistant	GNDEC,		
7	Kaur	Professor	Ludhiana		
	Gurjot	Assistant	GNDEC,		
8	Kaur	Professor	Ludhiana		
	Navneet	Assistant	GNDEC,		
9	Kaur	Professor	Ludhiana		
	Gurpurneet	Assistant	GNDEC,		
10	Kaur	Professor	Ludhiana		
		Assistant	GNDEC,		
11	Chahat Jain	Professor	Ludhiana		
	Haminder				
	Kaur	Assistant	GNDEC,		
12	Aulakh	Professor	Ludhiana		
	Kuldeepak	Assistant	GNDEC,		
13	Singh	Professor	Ludhiana		
	Jasmeet	Assistant	GNDEC,		
14	Kaur	Professor	Ludhiana		
	Sapanjot	Assistant	GNDEC,		
15	Kaur	Professor	Ludhiana		
	Simranjit	Assistant	GNDEC,		
16	Kaur	Professor	Ludhiana		
	Naazia	Assistant	GNDEC,		
17	Makkar	Professor	Ludhiana		
	Jaspreet	Assistant	GNDEC,		
18	Kaur	Professor	Ludhiana		

19	Nishu	Assistant	GNDEC,
		D C	
	Kansal	Professor	Ludhiana
	Kirandeep	Assistant	GNDEC,
20	Kaur	Professor	Ludhiana
	ASHISH	M.Tech	GNDEC,
21	THAKUR	Student	Ludhiana
	DAMAND	) ( m 1	ar Dec
	EEP	M.Tech	GNDEC,
22	KAUR	Student	Ludhiana
	GAGANP		
	REET	M.Tech	GNDEC,
23	KAUR	Student	Ludhiana
	GURLEEN	M.Tech	GNDEC,
24	KAUR	Student	Ludhiana
	PRABHJO	M.Tech	GNDEC,
	T KAUR	Student	Ludhiana
	PULKIT	M.Tech	GNDEC,
26	JAIN	Student	Ludhiana
	RAVNEET	M.Tech	GNDEC,
27	KAUR	Student	Ludhiana
	SAHAJPA	M.Tech	GNDEC,
28	L SINGH	Student	Ludhiana
	SAMJOT		
	KAUR	M.Tech	GNDEC,
29	AUJLA	Student	Ludhiana
	SAROOP	M.Tech	GNDEC,
30	SINGH	Student	Ludhiana
	SHEENA		
	M	M.Tech	GNDEC,
31	RATTAN	Student	Ludhiana
	AMANDE	M.Tech	GNDEC,
32	EP KAUR	Student	Ludhiana
	AVNEET	M.Tech	GNDEC,
33	KAUR	Student	Ludhiana
	GAGAND		
	EEP	M.Tech	GNDEC,
34	KAUR	Student	Ludhiana
	GAURAV		
	PRABHAK	M.Tech	GNDEC,
35	AR	Student	Ludhiana
	JOBANME		
	ET KAUR	M.Tech	GNDEC,
36		Student	Ludhiana

	KANWAR	) ( m 1	CAIDEC
	PREET	M.Tech	GNDEC,
37	KAUR	Student	Ludhiana
	NAVNEET		GNDEC,
38	KAUR	Student	Ludhiana
	NITIKA	M.Tech	GNDEC,
39	SHARMA	Student	Ludhiana
	RIPJEET		
	SINGH		
	DHALIWA		GNDEC,
40	L	Student	Ludhiana
	SARVESH	M.Tech	GNDEC,
41	SINGH	Student	Ludhiana
	SATYAM	M.Tech	GNDEC,
42	SHUKLA	Student	Ludhiana
	Prabhjot	B.Tech	GNDEC,
43	Kaur	Student	Ludhiana
	Sanjana	B.Tech	GNDEC,
44	Arora	Student	Ludhiana
	Nivedita	B.Tech	GNDEC,
45	Bhardwaj	Student	Ludhiana
		B.Tech	GNDEC,
46	Baljit Singh	Student	Ludhiana
	Raghav	B.Tech	GNDEC,
47		Student	Ludhiana
	Harmanpre	B.Tech	GNDEC,
48	et	Student	Ludhiana
	Neha	B.Tech	GNDEC,
49	Verma	Student	Ludhiana
	Manisha	B.Tech	GNDEC,
50		Student	Ludhiana
	Lovpreet	B.Tech	GNDEC,
51	Pal	Student	Ludhiana
	Shubham	B.Tech	GNDEC,
52	devgan	Student	Ludhiana
	Sarabjot	B.Tech	GNDEC,
53	Singh	Student	Ludhiana
	Sahil	B.Tech	GNDEC,
54		Student	Ludhiana
	Gurmeet	B.Tech	GNDEC,
55	Singh	Student	Ludhiana

### Workshop on Producing Elegant Technical Research Reports Efficiently (January 19 – 20, 2016)

### **List of Participants**

S No	Name	Designation	Affiliation
11	Aman Kumar	Faculty	SLIET
2	Amandeep Verma	PG Student	GNDEC
3	Ameeta Seehra	Faculty	GNDEC
4	Amit Sagar	PG Student	GNDEC
5	Amritpal Kaur	PG Student	GNDEC
6	Anjali	PG Student	CU, Mohali
7	Anterpreet Kaur	PG Student	GNDEC
8	Antterpreet Kaur	PG Student	GNDEC
9	Anuradha	PG Student	CU, Mohali
10	Ardamanbir Singh	Faculty	GNDEC
11	Arvinder Singh	PG Student	GNDEC
12	Avtarpreet Singh	PG Student	GNDEC
13	Bhupinder Kaur Srao	Faculty	CU, Mohali
14	Bindia Tarika	Faculty	GNDEC
15	Charanjit Singh	PG Student	GNDEC
16	Dhruv Chander Pant	PhD Scholar	GNDEC
17	Ekta Gupta	Others	
18	Gaurav Gupta	PhD Scholar	NITTTR
19	Hanisha	PG Student	GNDEC
20	Hardev Singh	PG Student	GNDEC
21	Harjinder Kaur	PG Student	GNDEC
22	Harmandeep Singh	Faculty	SLIET
23	Harmandeep Singh Gill	PG Student	GNDEC
24	Harpreet Kaur Grewal	Faculty	GNDEC
25	Harsimranjit Singh	PG Student	GNDEC
26	Harsimranpal Kaur	PG Student	GNDEC
27	Harwinder Singh	PhD Scholar	LLPR Moga
28	Inderjit Singh Dhanoa	Faculty	GNDEC
29	Inderpreet Kaur Dhindsa	Faculty	GNDEC
30	Jagbir Singh	Faculty	GNDEC
31	Jagdeep Singh	Faculty	GNDEC
32	Jagdish Kamboj	Faculty	CU, Mohali
	Jasleen Kaur	PG Student	GNDEC
34	Jeevan Bhatia	PG Student	GNDEC
35	Jeevesh Kumar	PG Student	NITTTR
36	Kamal Kishor	PG Student	NITTTR
37	Kanwalpreet Sahni	Faculty	GNDEC
38	Kanwardeep Singh	PG Student	GNDEC
39	Lovedeep Singh	PG Student	GNDEC
	Lovepreet Singh	PG Student	GNDEC
100000000000000000000000000000000000000	Maninder Savra	PG Student	GNDEC

Prof. & Head of Civil Engg. Dept. G. N. D. E. College, LUDHIANA

Land State S	Name	Designation	Affiliation
42	Manjot Singh	PG Student	GNDEC
43	Manvir Singh	PG Student	GNDEC
44	Mehak Sood	PG Student	GNDEC
45	Milanpreet Kaur	PG Student	GNDEC
46	Mukesh Kumar	PG Student	GNDEC
47	Navneet Kaur	Faculty	BBSBEC
48	Navneet Kaur	Faculty	GNDEC
49	Navneet Kaur	PG Student	GNDEC
50	Neeraj Bhanot	PhD Scholar	IITD
51	Pankaj Bhambri	Faculty	GNDEC
52	Pardeep Singh	Faculty	GNDEC
53	Parminder Kaur Wadhwa	Faculty	GNDEC
54	Parminderjeet Singh	Faculty	GNDEC
55	Pawan Verma	PG Student	GNDEC
56	Prateek Malhotra	PG Student	NITTTR
57	Preetinder Singh Brar	Faculty	Chitkara
58	Rajinder Ghai	PhD Scholar	TU
59	Raman Preet	Faculty	LPKC
60	Ramandeep Sharma	PG Student	GNDEC
61	Randhir Singh	Faculty	GNDEC
62	Sakshi Dua	Faculty	LPKC
63	Satwinderjit Kaur	Faculty	GNDEC
64	Sayantan Ghosh	PG Student	NITTTR
65	Shahbaz Singh	PG Student	GNDEC
66	Simrat Kaur	Faculty	BBSBEC
67	Sukhpreet Kaur Gill	PG Student	GNDEC
68	Sumeet Kaur Sehra	Faculty	GNDEC
69	Sumeet Rehal	PG Student	GNDEC
70	Upasna Sharma	PG Student	GNDEC
71	Vijay Kumar	PG Student	GNDEC
72	Vikas Rattan	Faculty	Chitkara
73	Vivek Thapar	Faculty	GNDEC

sils,

Dr. H S Rai Coordinator, Workshop Producing Elegant Technical Research Reports Efficiently

Prof. & Head of Civil Engg. Depu. G. N. D. E. College. LUDHIANA

### NATIONAL INSTITUTE OF TECHNICAL TEACHERS' TRAINING & RESEARCH SECTOR 26, CHANDIGARH-160 019

### **DEPARTMENT OF APPLIED SCIENCE**

erm Course

Advances in OFC Technology

pril, 2016

Venue: Guru Nanak Dev Engg. College, Ludhiana – Pb.

OPLAN No. ID-3

	SHEET:													
Sr	Name with Designation	Name of Institution	-	5/04	26	/04	-	/04	28/			/4/16	NAME IN HINDI	Gov
No	(in Block Letters)	(in Block Letters)	M	E	M	E	M	E	M	E	M	E		
1.	· PROFESSOR		5	1			D'	S	10	D.	(C)	NO Y	पदमा देवी	Pv9. 84272
2.	Harninder Dano	GNDEC, Ldh.	<b>P</b>	P			Apr	A	A	M	P	A	हरानंदर नीर	Govt:
3	Rajan Miglani	LPU.	E	Si	1	Q.	2	8	2	L		6	विजिल भिगलानी	Put. 97498
У.	Parambeel Singh	LP. U.	V	2	Ð	0	8	0	B	A	12	Q	परमदीय सिंह	99882
5.	Guejot Snigt,	L.P. U., Jalandha	Of the	671	ant st	dj	Cay	Cail 811	對	(d)	37	184	गुरजीत सिंह	98888
6	Chahat Jain	GNDEC, Ldh	fer	fer.	fér	折	Ju	Jú	Ju	A	Jur	for	चाद्त अन	Govt. 7837
7.	Gurpurneet Kaur	GNDEC, rdh	<b>Bar</b>	Qu	000		Addi	OF.	Dar	<b>P</b>	0	O.	गुरपुरतीत करि	99.888 Govt
8.	Ravneet kaur	G.N.D. E.C., Kolh	But	Row	gans	80.	بعدا	Ru.	for!	Jones	for	ef ans	रवनीत कौर	95011
9.	BINDIA TARIKA	GNDEC, WL	elle	et le	dile	gl'}	Ri	8:10	B.7	di	Re (N)	×	विधिया तारिका	90230
10	branjat Kaur.	LCET, Ldh.	Aug.	Jay	Pay		(Ser	Par.	27/0	of or	16	in live	अमन जीत भी र	82281 BAL

### NATIONAL INSTITUTE OF TECHNICAL TEACHERS' TRAINING & RESEARCU SECTOR 26, CHANDIGARII-160-019

### DEPARTMENT OF APPLIED SCIENCE

same of Short Term Course

Advances in OFC Technology

W.E.F. 25-29 April, 2016

Venue: Guru Nanak Dev Engg. College, Ludhiana - Pb.

OPLAN No. 1D-3

J. W.	E.F. 25-29 April, 2016	Venue: Guru Nanak De	ev Engg. College, Ludhiana – Pb. OPLAN No. 11	7-,
AT	TENDANCE SHEET:			
Sr. No.		Name of Institution (in Block Letters)	25/04   26/04   27/04   28/04   29/4/16   NAME IN HINDI   M   E   M   E   M   E   M   E	Govt
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### SECTOR 26, CHANDIGARH-160 019

### **DEPARTMENT OF APPLIED SCIENCE**

Name of Short Term Course

Advances in OFC Technology

W.E.F. 25-29 April, 2016

Venue: Guru Nanak Dev Engg. College, Ludhiana - Pb.

OPLAN No. ID-3

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	TTENDANCE SHEET:								
100	r. Name with Designation (in Block Letters)	Name of Institution (in Block Letters)	25/04 M E	26/04 M E	27/04 M E	28/04 M E	29/4/16 M E	NAME IN HINDI	Govt
80	GURJOT KAUR WALLA 1. AP, ECE REPTE. WALLA	GNDEC	A-2 00	201	S S	A 45	, ,	भुरजीतकौर वातीया	98881
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27	. Jaspreet Kaur AP, ECE	GNDEC, Ldh	Ja 12	加加	Ju . Ja	18 gr	JR M	जसप्रीत कीर	Govt.
28	AMEETA SEEHRA (ASSOC. PROF-ECE)	GNDEC, LDH.	KK	Jenks	1k of	1/2 1/2	1/2 /2	असीता सीह्रा	Gevt 18721
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30	MANJOT KAUR GILL (Assistant Perf., CSE)	GNIDEC, LDVI.	tor, My	Mar May	me, me	ive M	a Man Har	मनजीत कीर	94630

### NATIONAL INSTITUTE OF TECHNICAL TEACHERS' TRAINING & RESEARCH SECTOR 26, CHANDIG ARII-160 019

### DEPARTMENT OF APPLIED SCIENCE

Name of Short Term Course

Advances in OFC Technology

W.E.F. 25-29 April, 2016

Venue: Guru Nanak Dev Engg. College, Ludhiana - Pb.

OPLAN No. 1D-3

	E.F. 25-29 April, 2016 TENDANCE SHEET:	Venue: Guru Nanak Dev	SANT IN HIND	Go
Sr. No.	Name with Designation (in Block Letters)	Name of Institution (in Block Letters)	M E M E M E M E	
9	ASSISTANT PROFESSOR MEHTAS SINGH	SATYAM INSTITUTE OF ENOS RIFECH, AMRITSAR	The agent as my 10 methor has	P. A 195014
32	Assistant Professon Rajveen	SIET Amendsan	7/2 2/1 6/47	(UT 9988 God
33	favinder Kung.	GNOEC	2 72 2 7	is76
34	Tarandeep Kaur (i) dristant (referror	4-NDEC	Om din din And mon our in din Am 43-16/1. 3 5 4148	8072 601
31	Neha Assistant Professor	GILDEC	you top my in my my my my my my you	7799
36	Shivani Absol Assistant Professor	GNDEC	5 2 2 8 3 3 2 0 2	१९१८०
27	HARMEET SINGH GILL Assistant Professor	GNIDEC		(1)(
38	Harpeet kan Grewal Associate Prof.	GNBEC	the Horn the the the to see that also started and	7815
39	fandhir Singh	4NDEC		987
40.	Samreet Kaur Gosaf	GNDEC	ि के के के कि कि दामरोत कार गारिस	981

### NATIONAL INSTITUTE OF TECHNICAL TEACHERS: TRAINING & RESEARCH SECTOR 26, CHANDIGARH-160-019

### DEPARTMENT OF APPLIED SCIENCE

Name of Short Term Course Advances in OFC Technology W.E.F. 25-29 April, 2016 Venue: Guru Nanak Dev Engg. College, Ludhiana - Pb. OPLAN No. ID-3 ATTENDANCE SHEET: Name with Designation Sr. Name of Institution 25/04 26/04 27/04 28/04 29/4/16 NAME IN HINDI Gos No. M E (in Block Letters) ME (in Block Letters) M E NANZIA MAKKAR ताजीया क्षेत्रकड़ GNDEC, Ludhiana 41. 99151 ASSISTANT PROFESSOR Gout. GURMUKH SINGH 42. GNDEC, Ludhian gr 95011 LAR SUPDT. GOVI , ASUL SINGY GRIDEC, Luchiane No 36 36 9055 43 तस्वीर LAB. Tech. Govs GURPREET KAUR GILL 8968 GNDEC, Ldn. ASSISTANT PROFESSOR (10,1 Gurinder Singh 1 (5 T 99141 Katani Kalon Priva KANWALJEETKAUR besour saur AD LABTECH GNDEC Ldh Liur mit Singh ग्रमित गिर्ह GNOFC - LUL LABITECH. GAGANDEEP KAUR Jic 3131-7 2/4 4 N.DEC - Ldh 48 84273 LAB TECH 9878 GURSENGE SINGH GNDE C. Ldh 9878 LAB TECH. 50 SANDEEP SINGH 8 8 8 8 8 8 8 8 9 GNDEC Loll 9614.

LAB TECH.

### GURU NANAK DEV ENGINEERING COLLEGE, LUDHIANA

No. E/258

Dated: 29.4-16

Director

Sir.

33

Please allow following faculty members to attend TEQIP-II Sponsored Twelve days FDP on "Technology and Management for Electronic Product Design" organized by Department of Electronics & Communication Engineering on May 2,2016 to May 13, 2016. Recommendation/ forwarding from respective HODs has been received.

- 1. Er. Ameeta Seehra, Assoc.Prof,ECE
- 2. Er. Narwant Singh Grewal, Asstt. Prof.ECE
- 3. Er. Munish Rattan, Asstt. Prof.ECE
- 4. Er. Baljeet Kaur, Asstt. Prof, ECE
- 5. Er. Balwinder Singh Dhaliwal, Asstt. Prof,ECE
- 6. Er. Vivek Thapar. Asstt. Prof. CSE
- 7. Mrs. Amanjjot Kaur Gill , Asstt. Prof.MBA
- 8. Er. Prashant Garg. Asstt. Prof. CE
- 9. Er. Harminder Kaur, Asstt.Prof.ECE
- 10. Er. Gurjot Kaur Walia, Asstt. Prof,ECE
- 11. Er. Navneet Kaur, Asstt. Prof, ECE
- 12. Er. Kuldeepak Singh, Asstt. Prof.ECE
- 13. Er. Daljit Singh, Asstt. Prof,ECE
- 14. Er. Gurpuneet Kaur, Asstt. Prof,ECE
- 15. Er. Chahat Jain, Asstt. Prof, ECE
- 16. Er. Harminder Kaur Aulakh, Asstt. Prof,ECE
- 17. Er. Jasmeet Kaur. Asstt. Prof. ECE
- 18. Er. Sapanjot Kaur, Asstt. Prof, ECE
- 19. Er. Simranjit Kaur, Asstt. Prof, ECE
- 20. Er. Jaspreet Kaur, Asstt. Prof, ECE
- 21. Er. Naazia Makkar, Asstt. Prof. ECE
- 22. Er. Kirandeep Kaur, Asstt. Prof. ECE
- 23. Er. Nishu Kansal, Asstt. Prof. ECE
- 24. Er. Ranjodh Kaur, Asstt. Prof, IT
- 25. Mrs. Gagandeep Kaur, Asstt. Prof, App.Sci
- 26. Ms. Mandeep Kaur, Asstt. Prof, App.Sci
- 27. Ms. Rupinder Kaur, Asstt Prof. App.Sci
- 28. Er. Kanwalpreet Sahni, Asstt. Prof. Prod

MAN

Allowed/ Not allowed

086

Director

ARIAM DIAU

### GURU NANAK DEV ENGINEERING COLLEGE, LUDHIANA

Dated: 28.4.16

### Director

Sir.

Please allow following faculty members to attend TEQIP-II Sponsored Twelve days FDP on "Technology and Management for Electronic Product Design" organized by Department of Electronies & Communication Engineering on May 2,2016 to May 13, 2016. Recommendation/ forwarding from respective HODs has been received.

- Dr. D.S. Pathania, Professor (App. Sci.)
- Dr. Harpreet Kaur Grewal, Assoc.Prof (App.Sci.)
- Dr. Amanpreet Kaur Sodhi, Asstt. Prof (App.Sci.)
- S. Randhir Singh, Asstt. Prof (App.Sci.)
- Mrs. Pragya, Asstt. Prof (App.Sci.)
- Mrs. Puncet Narang, Asstt Prof. ( App.Sci.)
- Mrs. Rajbir Kaur, Asstt. Prof (App.Sci.)
- Dr Parminder Singh, Assoc Prof (CSI)
- Dr. Kulwinder Singh Mann, Assoc Prof (IT)
- 10. Er. Jasbir Singh Saini, Assoc.Prof (CC)
- 11. Dr. Amit Kamra, Asstt. Prof (IT) >
  - Dr. Kiran Jyoti, Asstt. Prof (IT)
  - Dr. Manpreet Singh, Asstt. Prof (11) / 13.
  - Dr. Ajay Shiv Sharma, Asstt. Prof (IT)
  - Er. Pankaj Bhambri, Asstt. Prf ( IT ) >
  - 16. Er. Kamaljit Kaur, Asst. Prof (1T) /
  - Er. Sandeep Kumar Singla. Assit. Prof ( 1T) /
  - 18. Er. Ranjit Singh, Asstt. Prof ( W/ Shop )
  - 19. Er. Ravinder Kaur, Asstt. Prof (EE) /
  - 20. Er. Harmeet Singh Gill, Asstt Prof (EE)
  - 21. S. Sukhminder Singh, Asstt Prof (App.Sci.) \*
  - 22. Mrs. Gurpreet Kaur, Asstt. Prof (App.Sci.) -
  - 23. Mrs. Harpreet Kaur, Asstt. Prof (App.Sci.)
  - 24. Mr. Karan Bhalla, Asstt. Prof (App.Sci.)
  - 25. Er. Rohit Sharma, Asstt. Prof (Prod)
  - 26. Er. Manjot Singh Bedi, Asstt. Prof ( Prod )
  - 27. Er. Manjot Kaur Gill, Asstt.Prof (CSE)
  - 28. Er. Mandeep Kaur, Asstt. Prof (CSE)
  - 29. Er. Amandeep Kaur, Assit. Prof (CSE)
  - 30. Er. Geetika, Asstt. Prof (CSE)
  - 31. Er. Gagandeep Kaur Gill, Assit Prof (CSE)
  - 32. Er. Kapil Sharma, Asstt. Prf ( CSE )
  - 33. Er. Shivani Abrol, Asstt. Prof (EE)
  - 34. Er. Gurpreet Kaur, Asstt. Prf (EE) /
  - 35. Er. Tarandeep Kaur, Asstt. Prof (EE)
  - 36. Er. Samreet Kaur, Asstt. Prof (EE)

  - 37. Er. Neha, Asstt. Prof (EE) /

Allowed/ Not allowed

### **Attendance Sheet** Research Methods for Engineers (31/05/2016 to 04/06/2016) Sr no Name Sign 1 Gurwant Singh Gill Gulvir Singh 2 aulines-3 Harnam Singh Farwaha 4 Jagjit Kaur 5 Chatwant Singh 6 Amrinder Singh Pannu 7 Suhel 8 Samriti 9 Pardeep Kumar 10 Harjot 11 Pardeep 12 Kuldeep Kumar 13 Harjinder Singh 14 Harkomaldeep Samdeep Kaur 15 Harjit Singh 16 Harwinder Singh 17 18 Partap Singh Jagjot Singh 19 Rupinderjit kaur 20 21 Karanveer Sukhraj Singh 22

Dev Kumar

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27	Gurjant Singh	04
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30	Jagwinder Singh	FREN
31	Lata Kumari	11.12
32	Jatinder Singh	Solm
33	Riya Bansal	Riga
34	Pawandeep	Paradell
35	Jotpreet	Jeffret.
. 36	Kulvir Singh	Kulvu
37	Harkomal	Habourel
38	Jagjeevanpal	Jaguet
39	Jagatveer	Jageonin.
40	Jagdev Singh	Bingh
41	Gurjot Kaur	a. Kaunskarelly
42	Heena Malhotra	H. Malhaden
43	Sapna	Some.
44	Harwinder Singh	Hawinder -
45	Hardeep Kumar	Harden Kennan.
		I/I∎

# Solid Waste Management in Indian Cities – Issues and Concern

S.		
No.	Name	Organization
1	Dr. Debi Prasad Tripathy	NIT Rourkela
2	Ms. Prabhdeep Kaur	Gian Jyoti Group of Institution, Banur
3	Ms. Amanpreet Kaur	Chandigarh University, Gharuan, Mohali
4	Ms. Meenakshi	PEC University of
5	Jatayan Ms. Rishi Rana	Technology, Chandigarh Jaypee University of Information and Technology, Wakhnaghat, Solan, Himachal Pradesh
6	Mr. Madan Kumar	Municipal Council, Mehatpur, Distt. Una (HP)
7	Mr. Karamjot Singh	BBSBEC, Fatehgarh Sahib
8	Mr. Puneet Sharma	Chandigarh University, Gharuan, Mohali
9	Mr. Deepak Kumar	Guru Ram Das Institute of Engineering & Technology, Lehragaga, Bathinda
10	Mr. Tarun Sharma	Chandigarh University, Gharuan
11	Ms. Kirti Goyal	Lovely Professional University, Jalandhar
12	Mr. Anoop Bhardwaj	Lovely Professional University, Jalandhar
13	Dr. Maninder Pal Singh	GNDU, RC, Sathiala
14	Ms. Prabhjot Kaur	GNDEC, Ludhiana
15	Ms. Bindia Tarika	GNDEC, Ludhiana
16	Ms. Ravneet kaur	GNDEC, Ludhiana
17	Mr. Ranjodh Singh	DAV University, Jalandhar
18	Mr. Rohin Kaushik	DAV University, Jalandhar
19	Mr. Sukhjit Singh Seehra	GNDEC, Ludhiana
20	Mr. Manpreet Singh Bedi	DAVIET, Jalandhar
21	Mr. Arshjot Singh	BBSBEC, Fatehgarh Sahib
22	Mr. Kulwant Singh	Punjabi University Campus, Damdama Sahib

#### Sheet1

		Y
23	Ms. Heena Malhotra	PEC University of
23	ivis. Heema iviamotra	Technology, Chandigarh
24	Mr. Pardeep Singh	GNDEC, Ludhiana
25	Mr. Kashish Jain	GNDEC, Ludhiana
26	Mr. Kulwinder Singh	GNDEC, Ludhiana
27	Ms. Inderpreet Kaur	GNDEC, Ludhiana
28	Mr. Gurdeepak Singh	GNDEC, Ludhiana
29	Mr. Prashant Garg	GNDEC, Ludhiana
30	Ms. Rajinderpal kaur	GNDEC, Ludhiana
31	Mr. Amandeep Singh Gill	GNDEC, Ludhiana
32	Mr. Prabhjot Singh Pannu	GNDEC, Ludhiana
33	Mr. Amandeep Singh	GNDEC, Ludhiana
34	Mr. Charnjeet Singh	GNDEC, Ludhiana
35	Ms. Gagandeep Kaur Grewal	GNDEC, Ludhiana
36	Mr. Yuvraj Singh	GNDEC, Ludhiana
37	Ms. Pritpal Kaur Gill	GNDEC, Ludhiana
38	Ms. Mandeep Kaur	GNDEC, Ludhiana
39	Ms. Tanu	GNDEC, Ludhiana
40	Mr. Bhupinder Singh	GNDEC, Ludhiana
41	Mr. Gurpuneet Singh	GNDEC, Ludhiana

# Workshop on "Effective Teaching Pedagogy and Desearch Methodology" on Harch 17-18, 2017

## Receipt of Certificates:

NO	Name of the Candidate	Signature .
NO.		180
	Dr. Kirm Jiphi	000
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	Kamaljit Kaue Dillon	The state of the s
3.	Manjot Ram Gill	July
4.	Chalint Lain	dr.
5.	Navneet Kaur	Clien
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QIP sponsored STC by IT Deptt. From 7/3/18 to 11/3/18

Name	Session 1	Session 2	Session 3	Session 4
Priyanka Arora	8K	AR V	1	&V
Mandeep Singh	MSL	MRI	mS1	MER
Gagandeep Kaur	Magan	yagan	ljagan	ljagan
Jasmine Kaur	Jaswin Kall	Jasuda	Januni	Jasulin Kans
Purnima Bakshi	TRE	(PB	PB	(PB
Vimal Vaiwala	amon	amon	aman	amel
Bhalwinder Kaur	Bholwinderkun	Bhelwiderkan	Bhelivudeskan	Bhelionaleylay
Divyadeep Kaur	Diwadey	Divyad	Divyast	Diman
Maninder kaur	Minder Kan	Marinder Kan	· Maufider Kaus	Havinder Keur
Taranjit kaur	Tananjit Kam			
Gurwinder Kaur	Gran	Gram	Grani	Gram
Nancy	Nany	Waney	naney	Nanuy
Kamal Nain Sharma	Kamal	Kaumal	Kamal	James
Gurpreet Kaur	GuoPoert	Gurpreet	Gurpocet	Gurpreet
Gayatri	Grayatri	Gayatri	Georgaty!	Grantetri"
Hardeep Kaur	Hardeel Kaus	( )	Hardee Kaus	Hardeap Kamp
Mehak	Mehale.	Mehak.	Mehal.	Mehok.
Dinesh Anand	A	A	2	2
Manan Kawatra	Manay	Manag	Klanon	Mars
Divya Sharma	Dirugnos	Dhuya	Drugg	Druy grave
Hardeep Singh Jawanda	C	67	Ah	AT
Kriti Verma	Kaiti Verma	Kriti Verme	Koiti Verme	Katti verma
Ashima Malhotra	88 millions	I Shine Letre	3/8hinallate	Krin verma
Surwinder Singh	(B) Angl	Odref	Colored	(C) fact

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K S Mann	W.	W	March (Mr.)	XXXXX
Manpreet Singh	Mach	Much	Much	Mul
Amit Kamra	A	A	An	A
Kamaljit Kaur Dhillon	KGT	Klix	12-6.8	KG8
Pankaj Bhambri			,	
Kanwardeep Singh	phyl	KH	the last	Porth
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Shaminder Singh	SL	SI	EL.	SI
Hitika Gupta	Hitinagupta	Hiting Gupta	Hitina Gusta	Hillipa Gutta
Navtej Singh Ghuman	1200	17 AD OIL	Warley St	Nil

#### AICTE-ISTE one week induction/ refresher course on "Advances in Antenna Theory and Techniques" (3.12.2018 – 8.12.2018)

	(3.12.2018		,
	Name of	Designatio	Institute
S. No.	Participant	n	Name
	Narwant		
	Singh	Assistant	GNDEC,
1	Grewal	Professor	Ludhiana
	Baljeet	Assistant	GNDEC,
2	Kaur	Professor	Ludhiana
	Gagandeep	Assistant	YCOE Talwandi
3	Kaur	Professor	Sabo
			YCOE
		Assistant	Talwandi
4	Sunita Rani	Professor	Sabo
	Manpreet	Assistant	YCOE Talwandi
5	Kaur	Professor	Sabo
	Ghanshym	Assistant	FGIET Rae
6	Singh	Professor	Bareli
	Dhawan	Assistant	Eternal
7	Singh	Professor	University
	Sunderjeet	Assistant	
8	Singh Gill	Professor	BCET Bhuta
	Suman	Associate	SSIET
9	Pattnaik	professor	DeraBassi
	ChintuRza	Assistant	
10	Makkar	Professor	DAVIET
		Associate	GCET,
11	Ajay Abrol	professor	ChakBhalwal, Jammu
1.1	Shivani	Assistant	
12	Mehta	Professor	DAVIET, jalandhar
	Gurinder	Assistant	GNDEC,
13	Singh	Professor	GNDEC, Ludhiana
	Gurinder	Assistant	LCET Katani
14	Singh	Professor	Kalan
		Assistant	GNDEC,
15	Chahat Jain	Professor	Ludhiana
		Assistant	GNDEC,
16	Daljit Singh		Ludhiana
	, ,		

	kar	Assistant Professor	GNDEC, Ludhiana
18	Tarandeep	Assistant	GNDEC,
	Singh	Professor	Ludhiana
19	Avneet	Assistant	GNDEC,
	Kaur	Professor	Ludhiana
20	Rohan Gupta	Assistant Professor	JMIT Radaur
21	Gautam	Assistant	JMIETI
	Gupta	Professor	Radaur
22	Deepika	Assistant Professor	GGI, Khanna

LIST OF PARTICIPANTS

S.N.		iption
1	Name of College/Institute & Address	Guru Nanak Dev Engineering College, Gill Road, Ludhiana.
2	Short Term Course Title	Green Building and Techniques
3	STC O.Plan Number	CE-25
4	Duration of Course	From 17/12/2018 To 21/12/2018
5	Name, Designation of Local Course Coordinator	Dr. Prashant Garg, Assistant Professor
6	Mobile No. & Email-id of Local Course Coordinator	9914313435

Sr.	Name & Designation of	Official Address of the Institute
No.	Participant (in Block Letters)	(in Block Letters)
1.	Gagandeep Kaur Grewal,	GURU NANAK DEV ENGINEERING
	Assistant Professor	COLLEGE, LUDHIANA
2.	Sandeep Kaur,	GURU NANAK DEV ENGIŅEERING
	Assistant Professor	COLLEGE, LUDHIANA
3.	Pritpal Kaur,	GURU NANAK DEV ENGINEERING
	Assistant Professor	COLLEGE, LUDHIANA
4.	Harpal Singh,	GURU NANAK DEV ENGINEERING
	Professor	COLLEGE, LUDHIANA
5.	Amandeep Singh Gill,	GURU NANAK DEV ENGINEERING
	Assistant Professor	COLLEGE, LUDHIANA
6.	Sukhwinder Singh,	GURU NANAK DEV ENGINEERING
W	Assistant Professor	COLLEGE,LUDHIANA
7.	Prashant Garg,	GURU NANAK DEV ENGINEERING
	Professor	COLLEGE,LUDHIANA
8.	Harmeet Singh,	GURU NANAK DEV ENGINEERING
540	Assistant Professor	COLLEGE, LUDHIANA
9.	Rajvir Kaur,	GURU NANAK DEV ENGINEERING
	Assistant Professor	COLLEGE, LUDHIANA
10.	Yuvraj Singh,	GURU NANAK DEV ENGINEERING
	Assistant Professor	COLLEGE, LUDHIANA
11.	Gurdeepak Singh,	GURU NANAK DEV ENGINEERING
	Associate Professor	COLLEGE, LUDHIANA .
12.	Sukhwinderpal Singh,	GURU NANAK DEV ENGINEERING
	Assistant Professor	COLLEGE, LUDHIANA
13.	Tanpreet Singh,	GURU NANAK DEV ENGINEERING
	Assistant Professor	COLLEGE,LUDHIANA
14.	Gurbir Kaur Jawanda,	GURU NANAK DEV ENGINEERING
	Assistant Professor	COLLEGE, LUDHIANA
15.	Manvinder Kingra,	GURU NANAK DEV ENGINEERING
	Assistant Professor	COLLEGE, LUDHIANA
16.	Bhupinder Singh Walia,	GURU NANAK DEV ENGINEERING
	Assistant Professor	COLLEGE, LUDHIANA
17.	Inderpreet Kaur,	GURU NANAK DEV ENGINEERING
	Assistant Professor	COLLEGE, LUDHIANA
18.	Tanu	GURU NANAK DEV ENGINEERING
1.0.	Assistant Professor	COLLEGE, LUDHIANA
19.	Mandeep Kaur,	GURU NANAK DEV ENGINEERING
15.	Assistant Professor	
1	ASSISTALL FIOLESSOL	COLLEGE, LUDHIANA

20.	Sahibdeep Singh Setia,	
10000	Assistant Professor	GURU NANAK DEV ENGINEERING
21.	Pankaj Bhambari,	COLLEGE.LUDHIANA
	Assistant Professor	GURU NANAK DEV ENGINEERING
22.	Rupinderjit singh,	COLLEGE.LUDHIANA
	Assistant Professor	GURU NANAK DEV ENGINEERING
23.	Baljeet Singh,	COLLEGE,LUDHIANA
	Assistant Professor	GURU NANAK DEV ENGINEERING
24.	Harvinder Singh,	COLLEGE, LUDHIANA
	Professor	GURU NANAK DEV ENGINEERING
25.	Ananta Sharma,	COLLEGE, LUDHIANA
	Assistant Professor	GULZAR GROUP OF INSTITUTES, KHANNA
26.	Navneet Singh Bhangu,	
	Assistant Professor	GURU NANAK DEV ENGINEERING
27.	Harmeet Singh Gill,	COLLEGE, LUDHIANA
	Assistant Professor	GURU NANAK DEV ENGINEERING
28.	Sukhwinder Singh,	COLLEGE,LUDHIANA
	Assistant Professor	GURU NANAK DEV ENGINEERING
29.	Charanjit Singh,	COLLEGE, LUDHIANA
20.	Assistant Professor	CHANDIGARH UNIVERSITY
30.		
50.	Pardeep Singh Joia,	GURU NANAK DEV ENGINEERING
31.	Assistant Professor	COLLEGE, LUDHIANA
31.	Gurjot kaur,	GURU NANAK DEV ENGINEERING
32.	Assistant Professor	COLLEGE, LUDHIANA
32.	Kuldeep Singh,	GURU NANAK DEV ENGINEERING
33.	Assistant Professor	COLLEGE, LUDHIANA
33.	Ajitpal Singh,	GURU NANAK DEV ENGINEERING
34.	Assistant Professor	COLLEGE, LUDHIANA
34.	Rajwinder Singh,	GURU NANAK DEV ENGINEERING
25	Assistant Professor	COLLEGE, LUDHIANA
35.	Jagdish Preet Singh,	GURU NANAK DEV ENGINEERING
200	Assistant Professor	COLLEGE, LUDHIANA
36.	Shahbaz Singh,	GURU NANAK DEV POLYTECHNIC
- 07	Assistant Professor	COLLEGE, LUDHIANA
37.	Brahmjot Singh,	GURU NANAK DEV POLYTECHNIC
	Assistant Professor	COLLEGE, LUDHIANA
38.	Dr. M.K. Kaushik	D.A.V. INSTITUTE OF ENGINEERING
	Assistant Professor	AND TECHNOLOGY, JALANDHAR
39.	Hardev Singh	G.N.A. UNIVERSITY, PHAGWARA
-	Assistant Professor	
40.	Ismitpal Singh	GURU NANAK DEV POLYTECHNIC
		COLLEGE, LUDHIANA
41.	B.S. Walia	GURU NANAK DEV ENGINEERING
		COLLEGE,LUDHIANA

Verified by:

Name & Signature of:
Local Co-ordinator Short Term Course
Assistant Professor
Civil Engineering Deptt.

Attendance Sheet (QIP Short Term Course on AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing During Dec. 31st 2018 – Jan. 04th 2019 by the Dept. of Info. Tech., GNDEC Ludhiana)

S. No.	Dr./ Er./ Mr./ Ms./ Mrs.	Participant Name	Designation	Affiliation	Signature with Date
1	Dr.	Amit Kamra	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	As sitizhe
2	Er.	Birpal Kaur	Assistant Professor	GNA University, Phagwara	
3	Er.	Dinesh Anand	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	Anonel
4	Er.	Divya Sharma	Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	Stuffnerm
5	Er.	Gitanjali	Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	Apple.
6	Er.	Gurjit Kaur	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	Company of the control of the contro
7	Er.	Gurjot Singh	Assistant Professor	Malwa College, Samrala	Guysser
8	Er.	Hanit Karwal	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	Chi
9	Er.	Harjot Kaur Gill	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	(nol
10	1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	Harpeet Kaur	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	Har
11	Er.	Harsimran Kaur	Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	Houseman fauc
12		Jagdeep Singh	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	B
13		Jasmine Kaur	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	Low
14		K.S. Mann	Professor	Guru Nanak Dev Engineering College, Ludhiana	- Lan

Attendance Sheet (QIP Short Term Course on AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing During

Dec. 31st 2018 – Jan. 04th 2019 by the Dept. of Info. Tech., GNDEC Ludhiana)

			Registration for	the Course on Bated 211222	
S. No.	Dr./ Er./ Mr./ Ms./ Mrs.	Participant Name	Designation	Affiliation	Signature with Date
15	Dr.	Kamaljit Kaur	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	V 60
16	Er.	Kanwalpreet Singh Sidhu	Assistant Professor	Bhutta College of Engineering and Technology, Ludhiana	0.11
17	Er.	Kapil Kumar	Assistant Professor	Lingaya's Vidyapeeth, Faridabad	Kapil Kump 17/18
18	Er.	Karamjit Singh Gill	Assistant Professor	Bhutta College of Engineering and Technology, Ludhiana	
19	Mr.	Karanbir Singh	CEO	Up2Mark Technologies Pvt. Ltd.	Kalanba sist
	Dr.	Kiran Jyoti	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	18
20	Er.	Mandeep Kaur	Assistant Professor	Khalsa College for Women, Ludhiana	man 12/18
21		Mandeep Kaur	Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	Mandelp des
22	Er.	Navpreet Kaur	Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	Nauprestant
23		Nidhi Sharma	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	Λ.
24			Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	
25	Er.	Nitish Sharma		Guru Nanak Dev Engineering College, Ludhiana	- Charles
26	Dr.	Pankaj Bhambri	Assistant Professor	Ludhiana	0 21.0/18
27	Ms.	Parminder Kaur	Assistant Professor		
28	B Er.	Prabhjot Kaur	Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	grasyol.

Attendance Sheet (QIP Short Term Course on AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing During Dec. 31st 2018 – Jan. 04th 2019 by the Dept. of Info. Tech., GNDEC Ludhiana)

			Registration for	rne Course on Dated 31.12.2010	
S. No.	Dr./ Er./ Mr./ Ms./ Mrs.	Participant Name	Designation	Affiliation	Signature with Date
29	Er.	Prabhjot Kaur	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	
30	Mr.	Puneet Singh	Director	Up2Mark Technologies Pvt. Ltd.	Pyneet Sivile
31	Er.	Rajni Puri	Assistant Professor	Gujranwala Guru Nanak Institute of Management & Technology, Ludhiana	Lagre
32	Er.	Ravinder Kaur	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	
33	Er.	Rupinder Kaur	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	atr
34	Er.	Sachin Bagga	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	tr
35	Er.	Samreet Kaur Gosal	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	0
36	Er.	Sandeep Kumar Singla	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	10 31.12.18
37	Er.	Sartinderpal Singh	AP (CP)/ CP, Computer Centre	Guru Nanak Dev Engineering College, Ludhiana	Cald
38		Shifa Grover	Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	Shiza Guou.
39		Shivani Arora Abrol	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	8
40		Sidharth Jain	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	2
		Sumeet Kaur Sehra	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	1
41	0.50	Sunanda Lal	Assistant Professor	Gujranwala Guru Nanak Institute of Management & Technology, Ludhian	

Attendance Sheet (QIP Short Term Course on AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing During Dec. 31st 2018 – Jan. 04th 2019 by the Dept. of Info. Tech., GNDEC Ludhiana)

S. No.	Dr./ Er./ Mr./ Ms./ Mrs.	Participant Name	Designation	Affiliation	Signature with Date
43	Er.	Sunil Aggarwal	Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	
44	Er.	Swaranjit Singh	Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	Swarond Lingh.
45	Ms.	Taranpreet Kaur	Assistant Professor	Mata Gujri College, Fatehgarh Sahib	Margatia 18
46	Dr.	V.J. Rai	Principal	Bhutta College of Engineering and Technology, Ludhiana	
47	Dr.	Vikas Singla	Assistant Professor	Malout Institute of Management & Information Technology, Malout	8
47	Er.	Yadvir Kaur	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	Yours

Name of the Course: AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing
Date: 31st December 2018 (Monday)

S.No.		Name of the Participant	Designation		Day 1 (Signatures)	
				Session-1.1 (11-12:30pm)	Session-1.2 (1:15-2:45pm)	Session-1.3 (3-4:30pm)
1	Dr.	Amit Kamra	Assistant Professor	A	, A.	A
-2	Er.	Birpal Kaur	Assistant Professor	0	Do	(Ce
3	Er.	Dinesh Anand	Assistant Professor	TA		72
4	Er.	Divya Sharma	Research Scholar	Diya suga	Dicyg Shan	Stuga Snar
5	Er.	Gitanjali	Research Scholar	Tolyali.	- Tryal	- Salmal.
6	Er.	Gurjit Kaur	Assistant Professor	17	la-	bor '
7	Er.	Gurjot Singh	Assistant Professor	and a	4826	581
8	Er.	Hanit Karwal	Assistant Professor	1ch	1 Oh	1 Ou
9	Er.	Harjot Kaur Gill	Assistant Professor	Ton.	per	the
10	Er.	Harpeet Kaur	Assistant Professor	Hambled Kor	Harpitat Kon	Morphut Kr
11	Er.	Harsimran Kaur	Research Scholar	Housemoran teus	Housemoran Laus	Housinvan Jours
12	Er.	Jagdeep Singh	Assistant Professor	The	T	An '
13	Er.	Jasmine Kaur	Assistant Professor	2000	, other	, Dan
14	Dr.	K.S. Mann	Professor	SP	1	
15	Dr.	Kamaljit Kaur	Assistant Professor	Kyr	wei8	1288
16	Er.	Kanwalpreet Singh Sidhu	Assistant Professor			1
17	Er.	Kapil Kumar	Assistant Professor	Karjil Kumar	Kapil Kuman	Lapil Lumas

Name of the Course: AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing Date: 31st December 2018 (Monday)

S.No.		Name of the Participant	Designation		Day 1 (Signatures)	
				Session-1.1 (11-12:30pm)	Session-1.2 (1:15-2:45pm)	Session-1.3 (3-4:30pm)
18	Er.	Karamjit Singh Gill	Assistant Professor			
19	Mr.	Karanbir Singh	CEO	Karanting	Caranging!	Karandi A.
20	Dr.	Kiran Jyoti	Assistant Professor	1)	0/	10
21	Er.	Mandeep Kaur	Assistant Professor	NK.	WY	MX O
22	Er.	Mandeep Kaur	Research Scholar	Mandler	Mandey	1) andely
23	Er.	Navpreet Kaur	Research Scholar	Kam	Cham	O four.
24	Er.	Nidhi Sharma	Assistant Professor	wide Brame,	Bug Farm	Wellig Laure
25	Er.	Nitish Sharma	Research Scholar			14.4
26	Dr.	Pankaj Bhambri	Assistant Professor	- Talon	- Farm	Lagran
27	Ms.	Parminder Kaur	Assistant Professor	A D_	(A)	10
28	Er.	Prabhjot Kaur	Research Scholar	N.		9
29	Er.	Prabhjot Kaur	Assistant Professor	grathiotica	Prabhjat K	Paddystker
30	Mr.	Puneet Singh	Director	Puncet Star	June Sour	Puncel 8in
31	Er.	Rajni Puri	Assistant Professor	- 02	9/	8
32	Er.	Ravinder Kaur	Assistant Professor		_	
33	Er.	Rupinder Kaur	Assistant Professor	dr	dy	Live
34	Er.	Sachin Bagga	Assistant Professor	Ja	2	the
35	Er.	Samreet Kaur Gosal	Assistant Professor			

Name of the Course: AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing Date: 31st December 2018 (Monday)

S.No.		Name of the Participant	Designation		Day 1 (Signatures	
4-1				Session-1.1 (11-12:30pm)	Session-1,2 (1:15-2:45pm)	Session-1.3 (3-4:30pm)
36	Er.	Sandeep Kumar Singla	Assistant Professor	100	NA .	184
37	Er.	Sartinderpal Singh	AP (CP)/ CP, Computer Centre	Cash	( with	Catha
38	Er.	Shifa Grover	Research Scholar	Shop Grown	Shi La Guorn	Shofa Grown.
39	Er.	Shivani Arora Abrol	Assistant Professor	<b>®</b> 1	8	B
40	Er.	Sidharth Jain	Assistant Professor	1	2	Y
41	Dr.	Sumeet Kaur Sehra	Assistant Professor	The state of the s	40	Br
42	Ms.	Sunanda Lal	Assistant Professor	Stell	flall	Stall
43	Er.	Sunil Aggarwal	Research Scholar			-
44	Er.	Swaranjit Singh	Research Scholar	Sucomplitsingn	Susconamort singh.	Swarmortsingh
45	Ms.	Taranpreet Kaur	Assistant Professor	Marin	Marcon	Monard
46	Dr.	V.J. Rai	Principal			
47	Dr.	Vikas Singla	Assistant Professor	952	82	842
48	Er.	Yadvir Kaur	Assistant Professor	Yadan	Yadvid	Yodyli

Name of the Course: AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing Date: 01st January 2019 (Tuesday)

Venue: Consultancy Seminar Hall, Guru Nanak Dev Engineering College Ludhiana

S.No.		Name of the Participant	Designation		Day 2 (S	Signatures)	Residence of the second
				Session-2.1 (09.00 <sub>7</sub> 10:30am)	Session-2.2 (10.45-12:15pm)	Session-2.3 (01.00-02:30pm)	Session-2.4 (02.45-
1	Dr.	Amit Kamra	Assistant Professor	19	4	ah	4
2	Er.	Birpal Kaur	Assistant Professor	6	6	£ .	As .
3	Er.	Dinesh Anand	Assistant Professor	70	77	A	
4	Er.	Divya Sharma	Research Scholar	Drug snarma	- O juje Sharma	Oingo Shoem	Orya sharme
5	Er.	Gitanjali	Research Scholar	- Julyala	- isterfali	Hajot.	Heijah
6	Er.	Gurjit Kaur	Assistant Professor	Charz	aux	Gran	Kar
7	Er.	Gurjot Singh	Assistant Professor	asile.	(hall	161	160
8	Er.	Hanit Kaur	Assistant Professor	10h	R	an	AX.
9	Er.	Harjot Kaur Gill	Assistant Professor	and	Total	act	ACC
10	Er.	Harpeet Kaur	Assistant Professor	Manphet Key	Howhite he	Harbirt K.	Horbit les
11	Er.	Harsimran Kaur	Research Scholar	Horsonian Lave	Haysonvan few	Houseman fau	Housimour fars
12	Er.	Jagdeep Singh	Assistant Professor	18	18	1	W
13	Er.	Jasmine Kaur	Assistant Professor	flare	a East	FIE	00
14	Dr.	K.S. Mann	Professor	W.			#
15	Dr.	Kamaljit Kaur	Assistant Professor	10-gg	4-60	V XIS	V 48
16	Er.	Kapil Kumar	Assistant Professor	Variat Lumas	Januay Lings	J. J. Yamar	Karly Luner

w

Name of the Course: AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing Date: 01st January 2019 (Tuesday)

S.No.	1	Name of the Participant	Designation		Day 2 (S	ignatures)	
				Session-2.1 (09.00-10:30am)	Session-2.2 (10.45-12:15pm)	Session-2.3 (01.00-02:30pm)	Session-2.4 (02.45- 04:15pm)
17	Mr.	Karanbir Singh	CEO	Kowles & 2	Law Sin	+ Lareltone	Karalegue
18	Dr.	Kiran Jyoti	Assistant Professor	2	10/	18	9
19	Er.	Mandeep Kaur	Assistant Professor	MK	MK	MK	MK
20	Er.	Mandeep Kaur	Research Scholar	Mary.	War.	Paper	ndo-
21	Er.	Navpreet Kaur	Research Scholar	Chair.	Pros	Popage	Mes
22	Er.	Nidhi Sharma	Assistant Professor	Listerent	Leabhard !	e Hollichers	Quality 1
23	Dr.	Pankaj Bhambri	Assistant Professor	- Rev-	The	tues	- Fory
24	Ms.	Parminder Kaur	Assistant Professor	00	-0	0	0
25	Er.	Prabhjot Kaur	Research Scholar	July of	a Ra	R	8
26	Er.	Prabhjot Kaur	Assistant Professor	Phabhotus	habbydh	Dashyot	Inappula
27	Mr.	Puneet Singh	Director	June et Stu	Yuncet 814	1 formet gy	Janes Fill
28	Er.	Rajni Puri	Assistant Professor	A	12	1 D	B
29	Er.	Rupinder Kaur	Assistant Professon	fix	Atk.	dix	dek
30	Er.	Sachin Bagga	Assistant Professor	1		10 Ju	200
31	Er.	Sandeep Kumar Singla	Assistant Professor	1 18	1	1/8	1 de la companya della companya della companya de la companya della companya dell
32	Er.	Sartinderpal Singh	AP (CP)/ CP, Computer Centre		1		
33	Er.	Shifa Grover	Research Scholar	Shila Grown	Shifa Grows	Ship Grany	Shita Gran

Name of the Course: AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing Date: 01st January 2019 (Tuesday)

S.No.		Name of the Participant	Designation	Day 2 (Signatures)					
				Session-2.1 (09.00-10:30am)	Session-2.2 (10.45-12:15pm)	Session-2.3 (01.00-02:30pm)	Session-2.4 (02.45- 04:15pm)		
. 34	Er.	Shivani Arora Abrol	Assistant Professor	A	A	8			
35	Er.	Sidharth Jain	Assistant Professor	22			4.5		
36	Dr.	Sumeet Kaur Sehra	Assistant Professor	*	R	25	1		
37	Ms.	Sunanda Lal	Assistant Professor	State	Stall	1(2)	10.00		
38	Er.	Swaranjit Singh		Swaria	Swarn	dumut-	Swarns		
39	Ms.	Taranpreet Kaur	Assistant Professor	* /	Mariant	Morran	Marie		
40	Dr.	Vikas Singla	Assistant Professor		and -	a D	(2)		
41	Er.	Yadvir Kaur	Assistant Professor		Madr	Yadir	7.		

Name of the Course: AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing Date: 02nd January 2019 (Wednesday)

S.No.		Name of the Participant	Designation		Day 3 (Si	gnatures)	
011.00				Session-3.1 (09.00-10:30am)	Session-3.2 (10.45-12:15pm)	Session-3.3 (01.00-02:30pm)	Session-3.4 (02.45 04:15pm)
1	Dr.	Amit Kamra	Assistant Professor	A	pr.	AS	A
2	Er.	Birpal Kaur	Assistant Professor			-	4
3	Er.	Dinesh Anand	Assistant Professor	a			
4	Er.	Divya Sharma	Research Scholar	Manny	Home	Axime	Manne
5	Er.	Gitanjali	Research Scholar	Thyal -	Control -	Carpet .	-til-ol
6	Er.	Gurjit Kaur	Assistant Professor	lyons	epus	gm.	gher.
7	Er.	Gurjot Singh	Assistant Professor	asm	Stat	gogul	Sher
8	Er.	Hanit Kaur	Assistant Professor	100	Cla	Ido	A COL
9	Er.	Harjot Kaur Gill	Assistant Professor	(A)	(t)of	Her	(he)
10	Er.	Harpreet Kaur	Assistant Professor	He	H2-	42	His
11	Er.	Harsimran Kaur	Research Scholar	Wear	Flor	(Has	Plan
12	Er.	Jagdeep Singh	Assistant Professor	· For	H	Jal	J.
13	Er.	Jasmine Kaur	Assistant Professor	- Sam	gran	Kara	Skape
14	Dr.		Professor			W.	
15	Dr.	Kamaljit Kaur	Assistant Professor		KFG8	KG8	Kgy
16	Er.	Kapil Kumar	Assistant Professor	Karil Lunar	Karil Kumar	Laried Luman	Kapel Kumus

Name of the Course: AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing Date: 02nd January 2019 (Wednesday)

S.No.		Name of the Participant		Our a Trainan		signatures)	
				Session-3.1 (09.00-10:30am)	Session-3.2 (10.45-12:15pm)	Session-3.3 (01.00-02:30pm)	Session-3.4 (02.45- 04:15pm)
17	Mr.	Karanbir Singh	CEO	Konewigen	Karelter	Karabee	- Koreky
18	Dr.	Kiran Jyoti	Assistant Professor	N/	18/	10/	Q o
19	Er.	Mandeep Kaur	Assistant Professor	MK	MK	MK	MK
20	Er.	Mandeep Kaur	Research Scholar	W	N	- CD	No.
21	Er.	Navpreet Kaur	Research Scholar	Okam	Pkaur	Pham	Chaus
22	Er.	Nidhi Sharma	Assistant Professor	Delillahas	Vadualhas	Yusahare	Kadushane
23	Dr.	Pankaj Bhambri	Assistant Professor	Tank	Lakery	Targen	Marilla Maria
24	Ms.	Parminder Kaur	Assistant Professor		0	0	0
25	Er.	Prabhjot Kaur	Research Scholar	San	llow	Acri,	Pkur
26	Er.	Prabhjot Kaur	Assistant Professor	Trabbala	Maldystu	Tradyet	Osebhall
27	Mr.	Puneet Singh	Director	Puncet di	Kunter of	Puncetj.	Puncet & M.
28	Er.	Rajni Puri	Assistant Professor	8	Ø	D.	2
29	Er.	Rupinder Kaur	Assistant Professor	17	12	JIL	13/
30	Er.	Sachin Bagga	Assistant Professor	Ju	41	0	TATE
31	Er.	~	Assistant Professor	40	J.	18	94/
32	Er.		AP (CP)/ CP, Computer Centre	4	&	10/2	184
33	Er.	Shifa Grover		Shifer Grown	Stufe beau	Physa buom	Shelo brown.

Name of the Course: AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing Date: 02nd January 2019 (Wednesday)

S.No.		Name of the Participant	Designation	Day 3 (Signatures)					
				Session-3.1 (09.00-10:30am)	Session-3.2 (10.45-12:15pm)	Session-3.3 (01.00-02:30pm)	Session-3.4 (02.45- 04:15pm)		
34	Er.	Shivani Arora Abrol	Assistant Professor	00	8	8_	8		
35	Er.	Sidharth Jain	Assistant Professor	2	9	4	( 2 )·		
36	Dr.	Sumeet Kaur Sehra	Assistant Professor	5	4	5	8		
37	Ms.	Sunanda Lal	Assistant Professor	flell	flell	flel	Sleet		
38	Er.	Swaranjit Singh	Research Scholar	Suarre	Swanz	Suaras	Surane		
39	Ms.	Taranpreet Kaur	Assistant Professor	of sent	Morena	Moonet	Mapeter		
40	Dr.	Vikas Singla	Assistant Professor	952	8	82	252		
41	Er.	Yadvir Kaur	Assistant Professor	*dd:	Yode	Yodan	Yoult		

Name of the Course: AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing Date: 03rd January 2019 (Thursday)

S.No.		Name of the Participant	Designation		Day 4 (S	ignatures)	
				Session-4.1 (09.00-10:30am)	Session-4.2 (10.45-12:15pm)	Session-4.3 (01.00-02:30pm)	Session-4.4 (02.45-04:15pm)
1	Dr.	Amit Kamra	Assistant Professor	A	A	A	A
2	Er.	Birpal Kaur	Assistant Professor	8	1	0	
3	Er.	Dinesh Anand	Assistant Professor	at D	3		A
4	Er.	Divya Sharma	Research Scholar	They & morma	Diny grass	Dingham	Derdans
5	Er.	Gitanjali	Research Scholar	The state of the s	Introdu -	- Carry	- CHILIN
6	Er.	Gurjit Kaur	Assistant Professor	Grans	grans	come	Mario
7	Er.	Gurjot Singh	Assistant Professor	GSIL	God	4 Sul	68M
8	Er.	Hanit Kaur	Assistant Professor	Illa	Os.		
9	Er.	Harjot Kaur Gill	Assistant Professor	AND	Alex	(409)	Det
10	Er.	Harpreet Kaur	Assistant Professor	Hh	AL	H2	
11	Er.	Harsimran Kaur	Research Scholar	Akart	Hause	Alaus	Hau
12	Er.	Jagdeep Singh	Assistant Professor	*	1	2	J.
13	Er.	Jasmine Kaur	Assistant Professor	Okam )	Skaper	okan	Maur
14	Dr.	K.S. Mann	Professor	A	1		
15	Dr.	Kamaljit Kaur	Assistant Professor		148	Kgr	148
16	Er.	Kapil Kumar	Assistant Professor	Wash Kuman	1 - 1 Kuman	1 - Kumar	1 - 1 Kuman

Name of the Course: AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing Date: 03rd January 2019 (Thursday)

S.No.		Name of the Participant	Designation		Day 4 (5	Signatures)	
		THE RESERVE OF THE PARTY OF THE		Session-4.1	Session-4.2	Session-4.3	Session-4.4
18	Dr.	Kiran Jyoti	Assistant Professor	Q/	Q	0	-10
19	Er.	Mandeep Kaur	Assistant Professor	MK	MK	MK	MIK
20	Er.	Mandeep Kaur	Research Scholar	mk.	ML.	Mk.	Mt.
21	Er.	Navpreet Kaur	Research Scholar	Chaus	okans	Okanz	Mans
22	Er.	Nidhi Sharma	Assistant Professor	Nidehama	N) shighanna	Hidelycamie	Haligrann
23	Dr.	Pankaj Bhambri	Assistant Professor	- fangur	Lagues .	fargus	- faguer
24	Ms.	Parminder Kaur	Assistant Professor	a D	A.Q.	QA.	Oby /
25	Er.	Prabhjot Kaur	Research Scholar		\$/		1
26	Er.	Prabhjot Kaur	Assistant Professor	Prabhydetaus	Preblyotkaur	Prablystyans	grathysteaus
27	Mr.	Puneet Singh	Director	Parest Si	funct gu	Puncet Si.	hunest Sys
28	Er.	Rajni Puri	Assistant Professor	0 &	28	1	18
29	Er.	Rupinder Kaur	Assistant Professor	der	atr	Lux	deta
30	Er.	Sachin Bagga	Assistant Professor		0 52	e at	Jef John
31	Er.	Sandeep Kumar Singla	Assistant Professor	1878		N. C.	8
32	Er.	Sartinderpal Singh	AP (CP)/ CP, Computer Centre	1	1	19	1-2
33	Er.	Shifa Grover	Research Scholar	Shifa Grown	Gula treou	Infa brown	Shifa Gre

Name of the Course: AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing Date: 03rd January 2019 (Thursday)

S.No.		Name of the Participant	Designation		Day 4	(Signatures)	
				Session-4.1	Session-4.2	Session-4.3	Session-4.4
35	Er.	Sidharth Jain	Assistant Professor	\$	41		. 92
36	Dr.	Sumeet Kaur Sehra	Assistant Professor	4	R	8	a
37	Ms.	Sunanda Lal	Assistant Professor	Stell	Slall	School	glatt
38	Er.	Swaranjit Singh	Research Scholar	dwares	Surve	Queone	Sum
39	Ms.	Taranpreet Kaur	Assistant Professor	Moseur	Morrison	Morrison	Morante
40	Dr.	Vikas Singla	Assistant Professor	962	as-	952	AL
41	Er.	Yadvir Kaur	Assistant Professor	You	4 odule	4 od-	Yell
012	Me.	Shi vani A Abed	A.P	3	2	9	8

Name of the Course: AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing
Date: 04th January 2019 (Friday)

S.No.		Name of the Participant	Designation		Day 5 (S	ignatures)	
				Session-5.1 (09.00-10:30am)	Session-5.2 (10.45-12:15pm)	Session-5.3 (01.00-02:30pm)	Session-5.4 (02.45-04:15pm)
1	Dr.	Amit Kamra	Assistant Professor	A	A	A	A
2	Er.	Birpal Kaur	Assistant Professor	1	1	\$	\$
3	Er.	Dinesh Anand	Assistant Professor	2		A-	
4	Er.	Divya Sharma	Research Scholar	Dingathama	Diryallane	Sityastrono	Stryaston
5	Er.	Gitanjali	Research Scholar	- Thurds -	They al,	The state of the s	- STI AND
6	Er.	Gurjit Kaur	Assistant Professor	gkam	gkavy	glan	gkay
7	Er.	Gurjot Singh	Assistant Professor	asal	61	atr	and a
8	Er.	Hanit Kaur	Assistant Professor	NX.		(Va)	
9	Er.	Harjot Kaur Gill	Assistant Professor	* AND	T ADS	Had	(A) of
10	Er.	Harpreet Kaur	Assistant Professor	you	He	14	Ite
11	Er.	Harsimran Kaur	Research Scholar	Dove	Hour	Plane	Haus
12	Er.	Jagdeep Singh	Assistant Professor	0 1	2	K	26
13	Er.	Jasmine Kaur	Assistant Professor	Jean	Jkain	Fram	fkam
14	Dr.	K.S. Mann	Professor	W-			
15	Dr.	Kamaljit Kaur	Assistant Professor		1458	KGX	KGY
16	Er.	Kapil Kumar	Assistant Professor	11 - 1 Vumar	1 :   Xumar	. : \ Kumer	1 in Kumar

Name of the Course: AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing Date: 04th January 2019 (Friday)

S.No.		Name of the Participant	Designation		Day 5 (S	Signatures)	
				Session-5.1	Session-5.2	Session-5.3	Session-5.4
18	Dr.	Kiran Jyoti	Assistant Professor	(8)	Q	4	. 0_
19	Er.	Mandeep Kaur	Assistant Professor	MK.	MK	MY	MK A
20	Er.	Mandeep Kaur	Research Scholar	MA .	A.	MA	40
21	Er.	Navpreet Kaur	Research Scholar	Okany	Clary	Phair	Planty
22	Er.	Nidhi Sharma	Assistant Professor	Nidhgranna	Maligharmer	Wheighaum	Meligrama
23	Dr.	Pankaj Bhambri	Assistant Professor	Lacy	-FRIN	A STAND	- Som
24	Ms.	Parminder Kaur	Assistant Professor	0	4		(II)
25	Er.	Prabhjot Kaur	Research Scholar	te	2		X.
26	Er.	Prabhjot Kaur	Assistant Professor	Praidyot Rain	Problyst kan	Prateyotkaur	Praseystkam
27	Mr.	Puneet Singh	Director	Punset 8	Piercet Su	Junet 8	funests
28	Er.	Rajni Puri	Assistant Professor		R	- R	R
29	Er.	Rupinder Kaur	Assistant Professor	dex	h	1	AIN
30	Er.	Sachin Bagga	Assistant Professor	in the	0,77	Sign	Ju Do
31	Er.	Sandeep Kumar Singla	Assistant Professor	1000	19	No.	
32	Er.	Sartinderpal Singh	AP (CP)/ CP, Computer Centre	1	13	13	1
33	Er.	Shifa Grover	Research Scholar	Rife Guorn	Chyla Grown	Strife Treown	Shifa treown

Name of the Course: AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing Date: 04th January 2019 (Friday)

S.No.		Name of the Participant	Designation		Day 5 (	Signatures)	
				Session-5.1	Session-5.2	Session-5.3	Session-5.4
35	Er.	Sidharth Jain	Assistant Professor	92	2	9	. 80_
36	Dr.	Sumeet Kaur Sehra	Assistant Professor	Bo	8	8	K
37	Ms.	Sunanda Lal	Assistant Professor	Stell	العلا	Stell	Stell
38	Er.	Swaranjit Singh	Research Scholar	Limon	8wannie	Sugnar	Summe
39	Ms.	Taranpreet Kaur	Assistant Professor	Margaret	Morena	Maria	Moren
40	Dr.	Vikas Singla	Assistant Professor	(A)	(4)	2007	85
41	Er.	Yadvir Kaur	Assistant Professor	Yadvis	Yadr	Hode	40-

## **Cosolidated Attendance Sheet (QIP-STC)**

Name of the Course: AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing 31st December 2018 (Monday) - 04th January 2019 (Friday)

Venue: Consultancy Seminar Hall, Guru Nanak Dev Engineering College Ludhiana

S.No.		Name of the Participant	Designation					
				31.12.2018	1.1.2019	2.1.2019	3.1.2019	4.1.2019
1	Dr.	Amit Kamra	Assistant Professor	A	A	A	A.	A
2	Er.	Birpal Kaur	Assistant Professor	8	\$	8_	-	-
3	Er.	Dinesh Anand	Assistant Professor	70		all	A.	
4	Er.	Divya Sharma	Research Scholar	Hirgashana	Dingatrano	Drugashana	Dirigestran	Miye Than
5	Er.	Gitanjali	Research Scholar	- Internal	- Jugali	- Inde	Silver!	Cally
6	Er.	Gurjit Kaur	Assistant Professor	granz	fykaurs	ejkaur	efaur	Marz
21	Er.	Gurjot Singh	Assistant Professor	and	Soul	Lot	Sold	for -
8	Er.	Hanit Kaur	Assistant Professor					
(9)	Er.	Harjot Kaur Gill	Assistant Professor	Ale	AUL	Med	Al	They
-10	Er.	Harpreet Kaur	Assistant Professor	An	Th	Hh	FIK	Me
11	Er.	Harsimran Kaur	Research Scholar	Alan	Plans	Plans	Herry	frais
12	Er.	Jagdeep Singh	Assistant Professor	X	The	H	H	1
13	Er.	Jasmine Kaur	Assistant Professor	- Jkaun	TRacy	Thank	Jkaun	Traun
14	Dr.	K.S. Mann	Professor	Of	W.	V	W.	14
15	Dr.	Kamaljit Kaur	Assistant Professor	x gol	KGT	KGT	X-G8	KG8
16	Er.	Kapil Kumar	Assistant Professor	Vail Humar	Varial Kimon	Karial Kumar	shand on	Varid ofm

Haranbir &

### **Cosolidated Attendance Sheet (QIP-STC)**

Name of the Course: AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing 31st December 2018 (Monday) - 04th January 2019 (Friday)

S.No.		Name of the Participant	Designation					
116	1			31.12.2018	1.1.2019	2.1.2019	3.1.2019	4.1.2019
18	Dr.	Kiran Jyoti	Assistant Professor	· Q/	Q/	0/	Q.	9
19	Er.	Mandeep Kaur	Assistant Professor	MK	MK	MK	MK	MK
20	Er.	Mandeep Kaur	Research Scholar	100	A	R	R	182
21	Er.	Navpreet Kaur	Research Scholar	Planz	Chaus	Chain	Planz	Okam
22	Er.	Nidhi Sharma	Assistant Professor	Wildhigranna	Nedhisham	Mussiam	Mylligrams	Margranes
23	Dr.	— Pankaj Bhambri	Assistant Professor	fair.	for .	feet.	Fre -	form.
24	Ms.	Parminder Kaur	Assistant Professor		0		<b>P</b>	Q
25	Er.	Prabhjot Kaur	Research Scholar	B	6	0	8	Ber
26	Er.	Prabhjot Kaur	Assistant Professor	Prabligat Kall	PrablyotSlaur		Problyst Kaun	
27	Mr.	Puneet Singh	Director	lunest &	funcit &	Punea S;	(west or	Larest 18th
28	Er.	Rajni Puri	Assistant Professor	, Q	No.	R	A	B
29	Er.	Rupinder Kaur	Assistant Professor	det	Atr	AIN	Atk	Afre
30	Er.	Sachin Bagga	Assistant Professor	1500	Ing	19	D'	20
31	Er.	Sandeep Kumar Singla	Assistant Professor	100	0/12		Mr.	1
32	Er.	Sartinderpal Singh	AP (CP)/ CP, Computer Centre	1	1	54	5	
33	Er.	Shifa Grover	Research Scholar	Rila Grown	Shita brown	Stife Guow.	Infabren	Rifa brown

### **Cosolidated Attendance Sheet (QIP-STC)**

Name of the Course: AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing 31st December 2018 (Monday) - 04th January 2019 (Friday)

Venue: Consultancy Seminar Hall, Guru Nanak Dev Engineering College Ludhiana

S.No.		Name of the Participant	Designation					
				31.12,2018	1.1.2019	2,1.2019	3.1,2019	4,1,2019
35	Er.	Sidharth Jain	Assistant Professor	L	2	2	2	D
36	Dr.	Sumeet Kaur Sehra	Assistant Professor	6	5	8	8	8
37	Ms.	Sunanda Lal	Assistant Professor	Shell	Slell	My	Shall	Sled
38	Er.	Swaranjit Singh	Research Scholar	Swarmentsman	Surproruit Singu	ngmittilmrawl	Swariand & Sman	Suson Hiron
39	Ms.	Taranpreet Kaur	Assistant Professor	Morrison	Marini	Maraut	Mariant	- Marsur
40	Dr.	Vikas Singla	Assistant Professor	80	- 72	- 82	8	DY2
41	Er.	Yadvir Kaur	Assistant Professor	Y advis	Yadvi	Yadui	Yoda	400
42	nus	. Shinani A. Absol	A.P	8	8		8 _	8

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QIP Short Term Course on AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing During Dec. 31st 2018 – Jan. 04th 2019 by the Dept. of Info. Tech., GNDEC Ludhiana

Receiving Slip of 16GB Pen Drive

S. No.	Dr./ Er./ Mr./ Ms./ Mrs.	Participant Name	Designation	Affiliation	Signature with Date
1	Er.	Birpal Kaur	Assistant Professor	GNA University, Phagwara	
2	Er.	Divya Sharma	Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	Dilya Sugar
3	Er.	Gitanjali	Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	
4	Er.	Gurjit Kaur	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	lyhous
5	Er.	Gurjot Singh	Assistant Professor	Malwa College, Samrala	asory tiling
6	Er.	Harsimran Kaur	Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	War 9/10/1 2019
7	Er.	Jasmine Kaur	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	Jargentes
8	Er.	Kapil Kumar	Assistant Professor	Lingaya's Vidyapeeth, Faridabad	Karil Kuman 17/17
9	Mr.	Karanbir Singh	CEO	Up2Mark Technologies Pvt. Ltd.	Jenani
10	Er.	Mandeep Kaur	Assistant Professor	Khalsa College for Women, Ludhiana	Hamy 1/19
11	Er.	Mandeep Kaur	Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	Man (11/19.
12	Er.	Navpreet Kaur	Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	Raupruttous 4/01/
13	Er.	Nidhi Sharma	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	Mide Shara
14	Ms.	Parminder Kaur	Assistant Professor	Khalsa College for Women, Ludhiana	Dan 14/1/19

QIP Short Term Course on AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing During Dec. 31st 2018 – Jan. 04th 2019 by the Dept. of Info. Tech., GNDEC Ludhiana

Receiving Slip of 16GB Pen Drive

S. No.	Dr./ Er./ Mr./ Ms./ Mrs.	Participant Name	Designation	Affiliation	Signature with Date
15	Er.	Prabhjot Kaur	Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	Prastype kam 4 To
16	Er.	Prabhjot Kaur	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	Parshot Kale
17	Mr.	Puneet Singh	Director	Up2Mark Technologies Pvt. Ltd.	Queet &
18	Er.	Rajni Puri	Assistant Professor	Gujranwala Guru Nanak Institute of Management & Technology, Ludhiana	1 Sprille
19	Er.	Sartinderpal Singh	AP (CP)/ CP, Computer Centre	Guru Nanak Dev Engineering College, Ludhiana	Got 4/1/19
20	Er.	Shifa Grover	Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	Stofa Grow 4/1/19
21	Er.	Shivani Arora Abrol	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	2 8
22	Dr.	Sumeet Kaur Sehra	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	841119
23	Ms.	Sunanda Lal	Assistant Professor	Gujranwala Guru Nanak Institute of Management & Technology, Ludhiana	1 4/10/2019
24	Er.	Swaranjit Singh	Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	Sugna 112019
25	Ms.	Taranpreet Kaur	Assistant Professor	Mata Gujri College, Fatehgarh Sahib	Ymauri
26	Dr.	Vikas Singla	Assistant Professor	Malout Institute of Management & Information Technology, Malout	B-4/2/19/19

Attendance Sheet (QIP Short Term Course on AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing During Dec. 31st 2018 – Jan. 04th 2019 by the Dept. of Info. Tech., GNDEC Ludhiana)

Photograph on Dated 31.12.2018

S. No.	Dr./ Er./ Mr./ Ms./ Mrs.	Participant Name	Designation	Affiliation	Remarks for the Photographer
1	Dr.	Amit Kamra	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	5156
2	Er.	Birpal Kaur	Assistant Professor	GNA University, Phagwara	5159
3	Er.	Dinesh Anand	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	5173
4	Er.	Divya Sharma	Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	5278
5	Er.	Gitanjali	Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	5281
6	Er.	Gurjit Kaur	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	5267
7	Er.	Gurjot Singh	Assistant Professor	Malwa College, Samrala	5157
8	Er.	Hanit Karwal	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	5168.
9	Er.	Harjot Kaur Gill	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	5176
10	Er.	Harpeet Kaur	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	5175
11	Er.	Harsimran Kaur	Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	5164
12	Er.	Jagdeep Singh	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	5153
13	Er.	Jasmine Kaur	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	5265
14	Dr.	K.S. Mann	Professor	Guru Nanak Dev Engineering College, Ludhiana	5273

Attendance Sheet (QIP Short Term Course on AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing During Dec. 31st 2018 – Jan. 04th 2019 by the Dept. of Info. Tech., GNDEC Ludhiana)

Photograph on Dated 31.12.2018

S. No.	Dr./ Er./ Mr./ Ms./ Mrs.	Participant Name	Designation	Affiliation	Remarks for the Photographer
15	Dr.	Kamaljit Kaur	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	5165
16	Er.	Kanwalpreet Singh Sidhu	Assistant Professor	Bhutta College of Engineering and Technology, Ludhiana	a
17	Er.	Kapil Kumar	Assistant Professor	Lingaya's Vidyapeeth, Faridabad	5158
18	Er.	Karamjit Singh Gill	Assistant Professor	Bhutta College of Engineering and Technology, Ludhiana	a
19	Mr.	Karanbir Singh	CEO	Up2Mark Technologies Pvt. Ltd.	5263
20	Dr.	Kiran Jyoti	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	5274
21	Er.	Mandeep Kaur	Assistant Professor	Khalsa College for Women, Ludhiana	5178
22	Er.	Mandeep Kaur	Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	5277
23	Er.	Navpreet Kaur	Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	5279
24	Er.	Nidhi Sharma	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	5268.
25	Er.	Nitish Sharma	Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	4
26		Pankaj Bhambri	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	5160
27	1	Parminder Kaur	Assistant Professor	Khalsa College for Women, Ludhiana	5161
28		Prabhjot Kaur	Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	5276

Attendance Sheet (QIP Short Term Course on AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing During Dec. 31st 2018 – Jan. 04th 2019 by the Dept. of Info. Tech., GNDEC Ludhiana)

Photograph on Dated 31.12.2018

		Photogra	iph on Dated 31.12.2016	
Dr./ Er./ Mr./ Ms./ Mrs.	Participant Name	Designation	Affiliation	Remarks for the Photographer
A STATE OF THE STA	Prabhjot Kaur	Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	5269
		Director	Up2Mark Technologies Pvt. Ltd.	5262
		Assistant Professor	Gujranwala Guru Nanak Institute of Management & Technology, Ludhiana	5172
		Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	X
		Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	5266
		Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	5154
			Guru Nanak Dev Engineering College, Ludhiana	X
		Assistant Professor	Guru Nanak Dev Engineering College, Ludhiana	5285
			Callege Ludbigge	5174
			Guru Nanak Dev Engineering College, Ludhiana	5283
			Guru Nanak Dev Engineering College, Ludhiana	5288
				5169
				5286
	the state of the s	Mr./ Ms./ Mrs.  Er. Prabhjot Kaur  Mr. Puneet Singh  Er. Rajni Puri  Er. Ravinder Kaur  Er. Rupinder Kaur  Er. Sachin Bagga  Er. Samreet Kaur Gosal  Er. Sandeep Kumar Singla  Er. Sartinderpal Singh  Er. Shifa Grover  Er. Shivani Arora Abrol  Er. Sidharth Jain  Dr. Sumeet Kaur Sehra	Dr./ Er./ Mr./ Ms./ Mrs.  Er. Prabhjot Kaur Assistant Professor  Mr. Puneet Singh Director  Er. Rajni Puri Assistant Professor  Er. Ravinder Kaur Assistant Professor  Er. Rupinder Kaur Assistant Professor  Er. Sachin Bagga Assistant Professor  Er. Samreet Kaur Gosal Assistant Professor  Er. Sandeep Kumar Singla Assistant Professor  Er. Sartinderpal Singh AP (CP)/ CP, Computer Centred  Er. Shivani Arora Abrol Assistant Professor  Er. Sidharth Jain Assistant Professor  Dr. Sumeet Kaur Sehra Assistant Professor	Mr./ Ms./ Mr. Prabhjot Kaur Assistant Professor Guru Nanak Dev Engineering College, Ludhiana Mr. Puneet Singh Director Up2Mark Technologies Pvt. Ltd.  Er. Rajni Puri Assistant Professor Guru Nanak Institute of Management & Technology, Ludhiana Er. Ravinder Kaur Assistant Professor Guru Nanak Dev Engineering College, Ludhiana Er. Rupinder Kaur Assistant Professor Guru Nanak Dev Engineering College, Ludhiana Er. Sachin Bagga Assistant Professor Guru Nanak Dev Engineering College, Ludhiana Er. Samreet Kaur Gosal Assistant Professor Guru Nanak Dev Engineering College, Ludhiana Er. Sandeep Kumar Singla Assistant Professor Guru Nanak Dev Engineering College, Ludhiana Er. Sandeep Kumar Singla Assistant Professor Guru Nanak Dev Engineering College, Ludhiana Er. Santinderpal Singh AP (CP)/ CP, Computer Centre Guru Nanak Dev Engineering College, Ludhiana Er. Shifa Grover Research Scholar Guru Nanak Dev Engineering College, Ludhiana Er. Shivani Arora Abrol Assistant Professor Guru Nanak Dev Engineering College, Ludhiana Er. Sidharth Jain Assistant Professor Guru Nanak Dev Engineering College, Ludhiana Er. Sidharth Jain Assistant Professor Guru Nanak Dev Engineering College, Ludhiana Er. Sumeet Kaur Sehra Assistant Professor Guru Nanak Dev Engineering College, Ludhiana

Attendance Sheet (QIP Short Term Course on AI, Big Data and HPC Aligned Applications - A Modern Approach of Computing During Dec. 31st 2018 – Jan. 04th 2019 by the Dept. of Info. Tech., GNDEC Ludhiana)

Photograph on Dated 31.12.2018

Dr./ Er./ Mr./ Ms./	Participant Name	Designation	Affiliation	Remarks for the Photographer
	Sunil Aggarwal	Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	X1
		Research Scholar	Guru Nanak Dev Engineering College, Ludhiana	5163
		Assistant Professor	Mata Gujri College, Fatehgarh Sahib	5162
3000000			Bhutta College of Engineering and Technology, Ludhiana	8
			Malout Institute of Management & Information Technology, Malout	5264
				5177
		Er. Sunil Aggarwal  Er. Swaranjit Singh  Ms. Taranpreet Kaur  Dr. V.J. Rai  Dr. Vikas Singla	Mrs.  Er. Sunil Aggarwal Research Scholar  Er. Swaranjit Singh Research Scholar  Ms. Taranpreet Kaur Assistant Professor  Dr. V.J. Rai Principal  Dr. Vikas Singla Assistant Professor	Mrs.  Er. Sunil Aggarwal Research Scholar Guru Nanak Dev Engineering College, Ludhiana  Er. Swaranjit Singh Research Scholar Guru Nanak Dev Engineering College, Ludhiana  Ms. Taranpreet Kaur Assistant Professor Mata Gujri College, Fatehgarh Sahib  Dr. V.J. Rai Principal Bhutta College of Engineering and Technology, Ludhiana  Dr. Vikas Singla Assistant Professor Malout Institute of Management & Information Technology, Malout  Assistant Professor Guru Nanak Dev Engineering College, Ludhiana

#### One week QIP Short Term Course on "Machine Learning Applications in Engineering" (February 04-08, 2019)

## Organized by Department of Electronics and Communication Engineering Guru Nanak Dev Engineering College, Ludhiana

## List of Candidates for participation

No.	Candidate	Organization	Designation/ Department	Address	Contact no.	E-mail ID	Signature
	Chiteraryan Kurnan Rai	7/17 Jalandhas	Student/ Ick	MIT Jalandhas	7280776271	Chirlson janksai egral dan	Dani
2	S-PardeepSingh	G CET Jammu	dectures, Mechanical	GCET Tammy	8713069184	settic pardceps unita mail	Chyladle
	Sahil Gupta	DKTGI	A?, EC E	DKTGI Rahon			Sen gre
*1	10 Briged	ENDER Led hima	AP. ELE	Lidhini	9272544317	brijetka. @  Judec er i	Dus
*	Par Vaca	ander Ludhiane	AP, ECE	GNDEC Leidhlane	9988825855	grupunethan O gudec se. in	Care
4	Mario from	GND(C)	Al, ECE	Judhans	8265844119	name thank	010-

	FAROCE	GNDIC	Pursuing M. Tein	G. N. D. E. C.	7er 6754521	dadu) yes	7-11
\$	Precti Panne	GNDEC	A-P	ECE dept	9818947360		Puch
9	Harminder	qwnte	AP	11		avialch, hourminder	
10	NARMANT SINGH GREWAL	GNDEC	AP	0.7	98550-187.3	Defination narwant &	d n
11	PALLAVI ARORA	GPC GTB GARH (book Polytechn	Lecturer	CSE dept	8559027000	Pallavigsoner 2191 a gmail com	has
12		college, GTB GARH)				()	
13	Liteshe Puri	GNDEC	Pensaing MTcch	CINDEC ECE dept	7087700406	peuciclipshe 400 gread-com	DE!
14	Kamalpreet Kausi	GNDEC	fecusing M. Tech	ENDEC ECE dept	6280325628	KKamalpreet 851 & Gradillom	Yourdpin't
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# One week TEQIP-III sponsored Faculty Development Programme on "Enabling Technologies for Wireless and Optical Networks" June10-15, 2019

### Organized by Department of Electronics and Communication Engineering Guru Nanak Dev Engineering College, Ludhiana

(b) Certificate Receiving

Date 15-06-2019

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